

TITLE	Page
Cover Sheet	1
Block Diagram	2
CPU-CLK/Control/MISC/PEG ,CPU-Memory	3,4
CPU-Power ,CPU-GND	5,6
SO_DDRIII DIMMA1&SO_DDRIII DIMMB1	7,8
PPT-PCI/E/DMI /CLK/USB20/USB30	9
PPT-SATA/HOST/GPIO/VGA/CCMOS	10
PPT-SMB/LPC/AUDIO/RTC/SPI	11
PPT-POWER,GND/NVRAM /CP STRAPS	12,13,14
PCIE Slot ,PCI Slot	15,16
SIO-Fintek F71868AD	17
AUDIO ALC887/VT1708S	18
LAN - RTL8111E/8105E	19
DVI /VGA/SATA Connector	20,21,22
USB2.0/3.0 Connector	23,24
FAN	25
VRM12 - UT501	26
UP6282 3-Phase+MOS CPU	27
UP6282 1-Phase+MOS GPU	28
UP1513 - VTT POWER	29
OP+MOS - SA POWER	30
UP1513 - DDR POWER	31
UP1513 - PCH POWER	32
ATX F_Panel/EMI /TPM	33
ACPI Controller UPI	34
XDP / Manual Parts	35
EMI CAP	36
ME POWER - UP1712	37

# MS-7798

m-ATX

Ver: 1.0(243 \* 225)

## Intel -MahoBay plamform B75

### CPU:

IVY bridge LGA1155

### System Chipset:

Panther Point B75

### Onboard Chip:

HD Audio Codec:ALC887 colay VTI1708S

LAN-RTL8111E colay8105E

SIO:Fintek F71868AD

Flash ROM: SPI 128 MB

### Main Memory:

DDRIII (1066/1333/1600MHz) \* 4 (Dual Channel)

### ACPI:

UPI

### PWM:

VRD12 -UT501 3+1 Phase

### Expansion Slots:

PCI Express (X16) Slot \* 1

PCI Express (X1 ) Slot \* 1

PCI Slot \* 1

Other: SATA3.0 x1+SATA2.0 x5 (PCH)

USB2.0 \*8

REAL USB3.0 \*2

FRONT USB3.0 \*2

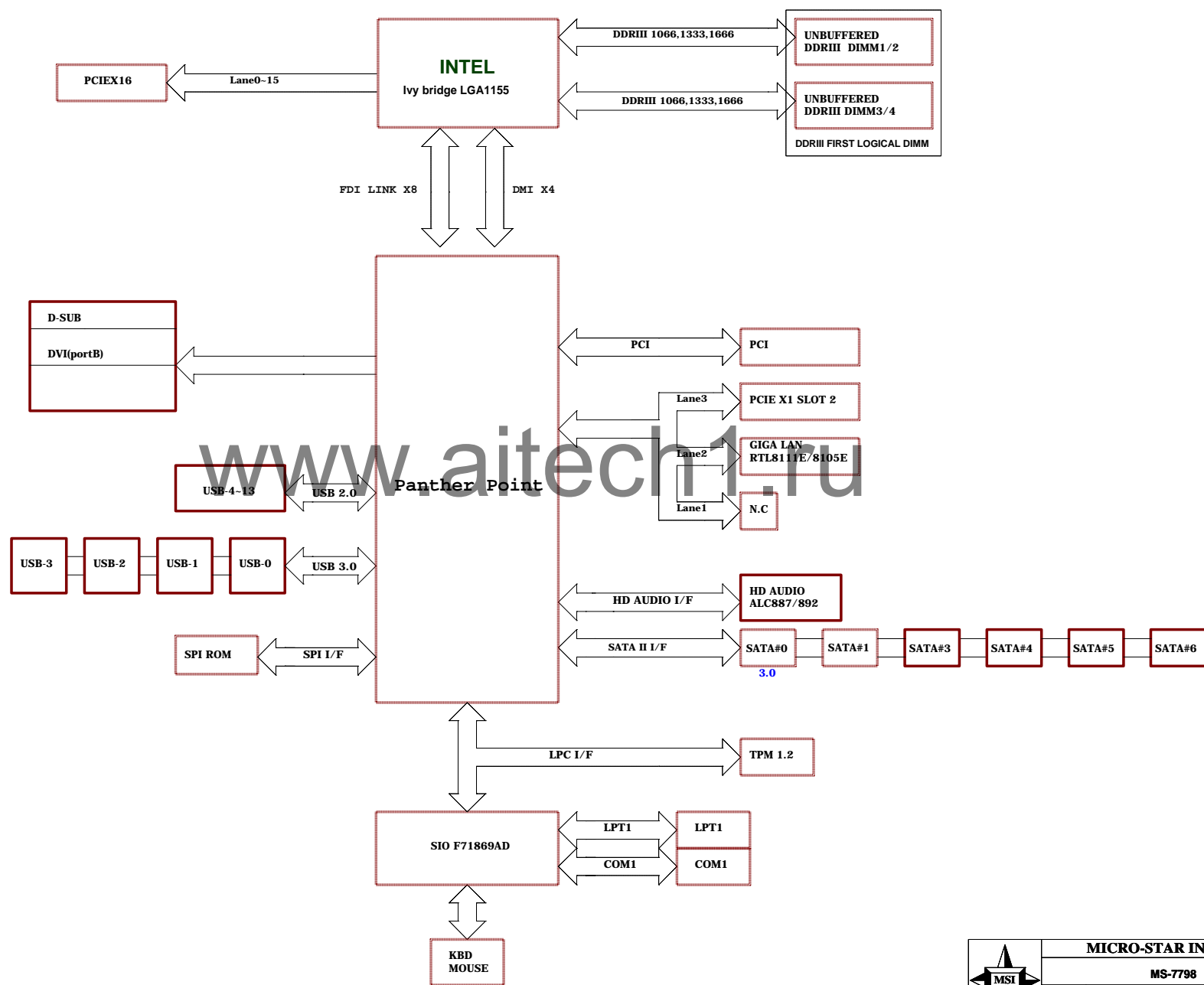


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MS-7798

Size	Document Description	Rev
Custom	Cover Sheet	1.0
Date: Monday, March 05, 2012	Sheet 1 of 41	

MS-7798 Block Diagram



Slot Sequence:

PCIE X16

PCIE X1

PCI







CPU1I  
9 OF 11

A17 VSS\_001 VSS\_091 AM27  
A23 VSS\_002 VSS\_092 AM3  
A26 VSS\_003 VSS\_093 AM30  
A29 VSS\_004 VSS\_094 AM37  
A35 VSS\_005 VSS\_095 AM38  
AA33 VSS\_006 VSS\_096 AM39  
AA34 VSS\_007 VSS\_097 AM40  
AA35 VSS\_008 VSS\_098 AM5  
AA36 VSS\_009 VSS\_099 AN10  
AA37 VSS\_010 VSS\_100 AN11  
AA38 VSS\_011 VSS\_101 AN14  
AA6 VSS\_012 VSS\_102 AN17  
AC1 VSS\_013 VSS\_103 AN19  
AC6 VSS\_014 VSS\_104 AN22  
AD33 VSS\_015 VSS\_105 AN24  
AD36 VSS\_016 VSS\_106 AN27  
AD38 VSS\_017 VSS\_107 AN30  
AD39 VSS\_018 VSS\_108 AN31  
AD40 VSS\_019 VSS\_109 AN32  
AD5 VSS\_020 VSS\_110 AN33  
ADR VSS\_021 VSS\_111 AN34  
AE3 VSS\_022 VSS\_112 AN35  
AE33 VSS\_023 VSS\_113 AN36  
AE36 VSS\_024 VSS\_114 AN5  
AF1 VSS\_025 VSS\_115 AN6  
AF34 VSS\_026 VSS\_116 AN7  
AF36 VSS\_027 VSS\_117 AN8  
AF37 VSS\_028 VSS\_118 AN9  
AF40 VSS\_029 VSS\_119 AP1  
AF6 VSS\_030 VSS\_120 AP14  
AF7 VSS\_031 VSS\_121 AP17  
AG36 VSS\_032 VSS\_122 AP22  
AH2 VSS\_033 VSS\_123 AP27  
AH3 VSS\_034 VSS\_124 AP30  
AH33 VSS\_035 VSS\_125 AP36  
AH36 VSS\_036 VSS\_126 AP40  
AH38 VSS\_037 VSS\_127 AP44  
AH39 VSS\_038 VSS\_128 AP45  
AH39 VSS\_039 VSS\_129 AP46  
AH40 VSS\_040 VSS\_130 AP47  
AH5 VSS\_041 VSS\_131 AP48  
AH6 VSS\_042 VSS\_132 AP49  
AH8 VSS\_043 VSS\_133 AP50  
AH12 VSS\_044 VSS\_134 AP51  
AH15 VSS\_045 VSS\_135 AP52  
AH18 VSS\_046 VSS\_136 AP53  
AJ21 VSS\_047 VSS\_137 AR14  
AJ25 VSS\_048 VSS\_138 AR17  
AJ27 VSS\_049 VSS\_139 AR18  
AJ36 VSS\_050 VSS\_140 AR22  
AJ6 VSS\_051 VSS\_141 AR27  
AK1 VSS\_052 VSS\_142 AR30  
AK10 VSS\_053 VSS\_143 AR36  
AK13 VSS\_054 VSS\_144 AT1  
AK14 VSS\_055 VSS\_145 AT10  
AK16 VSS\_056 VSS\_146 AT11  
AK22 VSS\_057 VSS\_147 AT12  
AK28 VSS\_058 VSS\_148 AT15  
AK31 VSS\_059 VSS\_149 AT16  
AK32 VSS\_060 VSS\_150 AT17  
AK33 VSS\_061 VSS\_151 AT25  
AK34 VSS\_062 VSS\_152 AT27  
AK35 VSS\_063 VSS\_153 AT28  
AK36 VSS\_064 VSS\_154 AT3  
AK37 VSS\_065 VSS\_155 AT30  
AK40 VSS\_066 VSS\_156 AT31  
AK5 VSS\_067 VSS\_157 AT32  
AK6 VSS\_068 VSS\_158 AT33  
AK7 VSS\_069 VSS\_159 AT34  
AK8 VSS\_070 VSS\_160 AT35  
AK9 VSS\_071 VSS\_161 AT36  
AL11 VSS\_072 VSS\_162 AT37  
AL14 VSS\_073 VSS\_163 AT38  
AL17 VSS\_074 VSS\_164 AT39  
AL19 VSS\_075 VSS\_165 AT40  
AL24 VSS\_076 VSS\_166 AT4  
AL27 VSS\_077 VSS\_167 AT5  
AL30 VSS\_078 VSS\_168 AT6  
AL36 VSS\_079 VSS\_169 AT7  
AL5 VSS\_080 VSS\_170 AT8  
AL5 VSS\_081 VSS\_171 AT9  
AM1 VSS\_082 VSS\_172 AU1  
AM11 VSS\_083 VSS\_173 AU15  
AM14 VSS\_084 VSS\_174 AU26  
AM17 VSS\_085 VSS\_175 AU34  
AM21 VSS\_086 VSS\_176 AU4  
AM2 VSS\_087 VSS\_177 AU8  
AM23 VSS\_088 VSS\_178 AU9  
AM25 VSS\_089 VSS\_179 AV10  
VSS\_090 VSS\_180

LGA1155

CPU1J  
10 OF 11

AV11 VSS\_181 H37  
AV14 VSS\_182 H39  
AV17 VSS\_183 H5  
AV3 VSS\_184 H9  
AV35 VSS\_185 H11  
AV38 VSS\_186 H17  
AV6 VSS\_187 H20  
AV10 VSS\_188 H23  
AV11 VSS\_189 H26  
AV14 VSS\_190 H29  
AV16 VSS\_191 H32  
AV36 VSS\_192 K1  
AW6 VSS\_193 K12  
AY11 VSS\_194 K13  
AY14 VSS\_195 K14  
AY18 VSS\_196 K17  
AY35 VSS\_197 K2  
AY4 VSS\_198 K20  
AY8 VSS\_199 K22  
B10 VSS\_200 K26  
B13 VSS\_201 K29  
B17 VSS\_202 K33  
B17 VSS\_203 K35  
B23 VSS\_204 K37  
B26 VSS\_205 K39  
B29 VSS\_206 K5  
B32 VSS\_207 K6  
B35 VSS\_208 K10  
B38 VSS\_209 K17  
B6 VSS\_210 L10  
C11 VSS\_211 L17  
C12 VSS\_212 L20  
C17 VSS\_213 L23  
C20 VSS\_214 L26  
C23 VSS\_215 L29  
C26 VSS\_216 L8  
C29 VSS\_217 L18  
C32 VSS\_218 M1  
C36 VSS\_219 M17  
C7 VSS\_220 M2  
C8 VSS\_221 M20  
D17 VSS\_222 M26  
D2 VSS\_223 M29  
D20 VSS\_224 M33  
D23 VSS\_225 M35  
D26 VSS\_226 M37  
D29 VSS\_227 M39  
D32 VSS\_228 M5  
D35 VSS\_229 M6  
D39 VSS\_230 M9  
D5 VSS\_231 N8  
D6 VSS\_232 N9  
D3 VSS\_233 N14  
E11 VSS\_234 P5  
E12 VSS\_235 P6  
E17 VSS\_236 P33  
E20 VSS\_237 P35  
E23 VSS\_238 P36  
E26 VSS\_239 P38  
E29 VSS\_240 P40  
E32 VSS\_241 P44  
E36 VSS\_242 P45  
E7 VSS\_243 P46  
E8 VSS\_244 P47  
F1 VSS\_245 P48  
F10 VSS\_246 P49  
F13 VSS\_247 P50  
F14 VSS\_248 P51  
F17 VSS\_249 P52  
F2 VSS\_250 P53  
F20 VSS\_251 P54  
F23 VSS\_252 P55  
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F29 VSS\_254 P57  
F36 VSS\_255 P58  
F37 VSS\_256 P59  
F39 VSS\_257 P60  
F5 VSS\_258 P61  
F6 VSS\_259 P62  
F9 VSS\_260 P63  
G11 VSS\_261 P64  
G12 VSS\_262 P65  
G17 VSS\_263 P66  
G20 VSS\_264 P67  
G23 VSS\_265 P68  
G26 VSS\_266 P69  
G29 VSS\_267 P70  
G34 VSS\_268 P71  
G7 VSS\_269 P72  
G8 VSS\_270 P73  
H1 VSS\_271 P74  
H17 VSS\_272 P75  
H2 VSS\_273 P76  
H20 VSS\_274 P77  
H23 VSS\_275 P78  
H26 VSS\_276 P79  
H29 VSS\_277 P80  
H33 VSS\_278 P81  
H35 VSS\_279 P82  
H35 VSS\_280

VSS\_NCTF\_01  
VSS\_NCTF\_02  
VSS\_NCTF\_03  
VSS\_NCTF\_04

LGA1155

(9) XDP\_CPU\_BCLK\_P  
(9) XDP\_CPU\_BCLK\_NXDP\_CPU\_BCLK\_P R122  
XDP\_CPU\_BCLK\_N R107CPU\_RSVD\_CLK\_P  
CPU\_RSVD\_CLK\_NCPU1K  
11 OF 11

C40 RSVD\_001  
D40 RSVD\_002  
AB6 RSVD\_003  
AB7 RSVD\_004  
AD37 RSVD\_005  
AE6 RSVD\_006  
AE4 RSVD\_007  
AG4 RSVD\_008  
AJ11 RSVD\_009  
AJ29 RSVD\_010  
AJ30 RSVD\_011  
AJ31 RSVD\_012  
AN20 RSVD\_013  
AP20 RSVD\_014  
AT11 RSVD\_015  
AT14 RSVD\_016  
AU10 RSVD\_017  
AV1 RSVD\_018  
AV34 RSVD\_019  
AW2 RSVD\_020  
AW34 RSVD\_021  
AY10 RSVD\_022  
B39 RSVD\_023  
C38 RSVD\_024  
C38 RSVD\_025  
C39 RSVD\_026  
D38 RSVD\_027  
H7 RSVD\_028  
H8 RSVD\_029  
J33 RSVD\_030  
J34 RSVD\_031  
J9 RSVD\_032  
K34 RSVD\_033  
K9 RSVD\_034  
L31 RSVD\_035

RSVD\_036  
RSVD\_037  
RSVD\_038  
RSVD\_039  
RSVD\_040  
RSVD\_041  
RSVD\_043  
RSVD\_044  
RSVD\_045  
RSVD\_046  
RSVD\_047  
RSVD\_048  
RSVD\_049  
RSVD\_050  
RSVD\_051  
RSVD\_052  
RSVD\_053

FC\_AH1  
FC\_AH4NCTF\_01  
NCTF\_02  
NCTF\_03  
NCTF\_04  
NCTF\_05

L33  
L34  
L9  
M34  
N33  
N34  
P35  
P37  
P39  
P43  
P44  
P46  
P47  
P48  
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P90  
P91  
P92  
P93  
P94  
P95  
P96  
P97  
P98  
P99  
P100

AH1 DIMM\_VREFB  
AH4 DIMM\_VREFAR370  
R368X OR  
X OROVREF\_DO\_B  
OVREF\_DO\_A

close to DIMM



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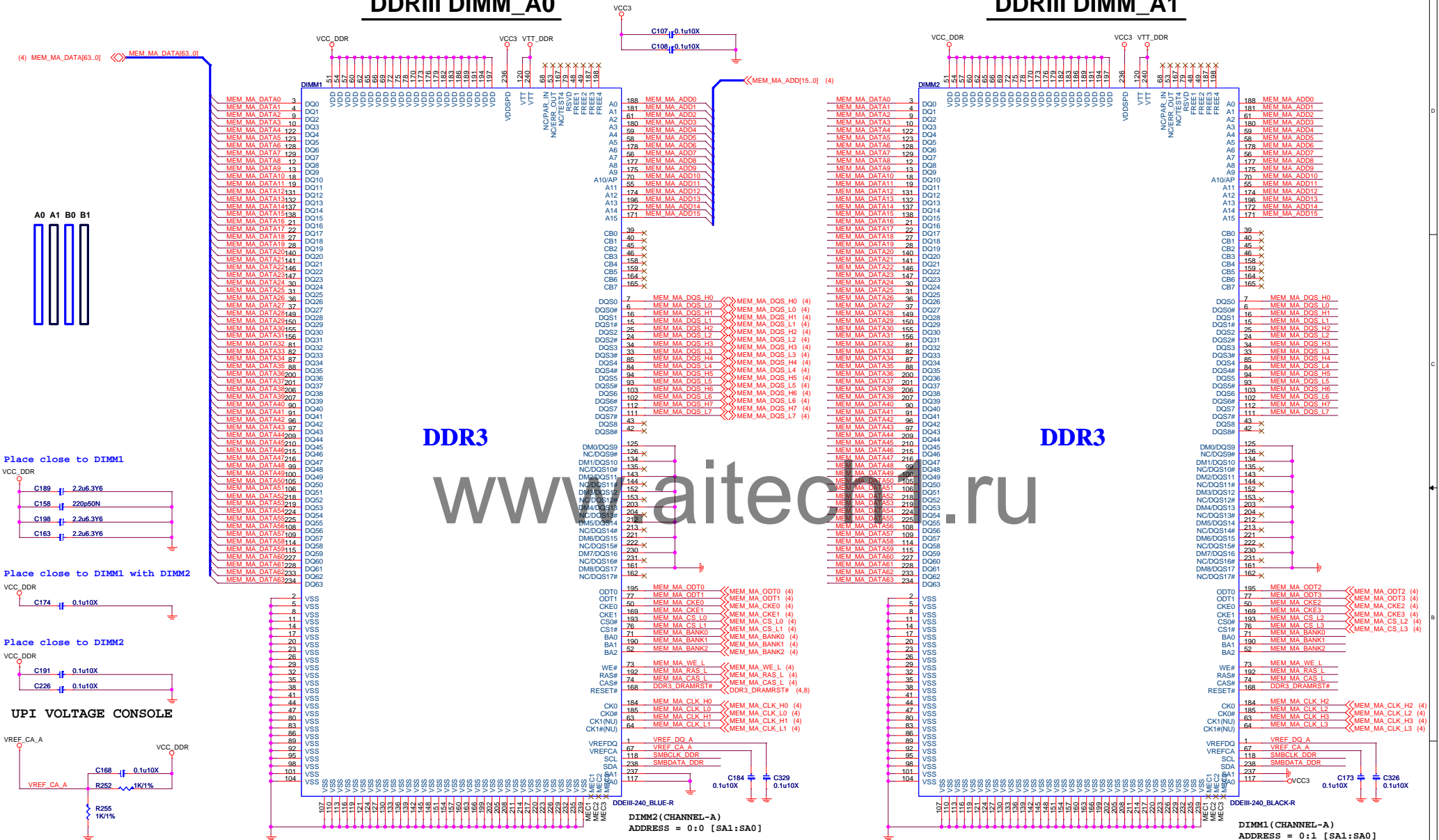
MS-7798

Size	Document Description	Rev
Custom	CPU-GND	1.0
Date: Monday, March 05, 2012	Sheet 6 of 41	



# DDRIII DIMM\_A0

# DDRIII DIMM\_A1

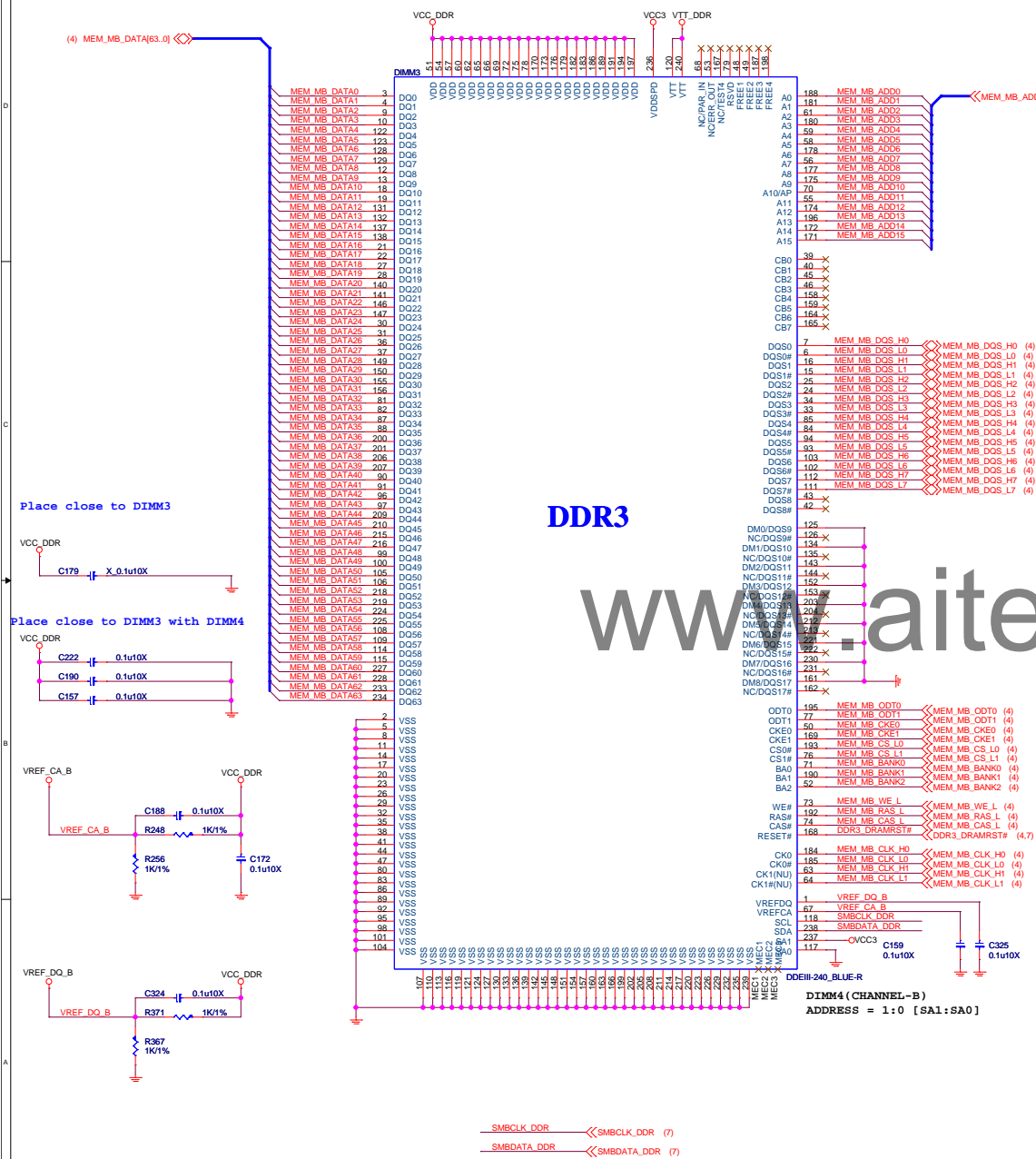


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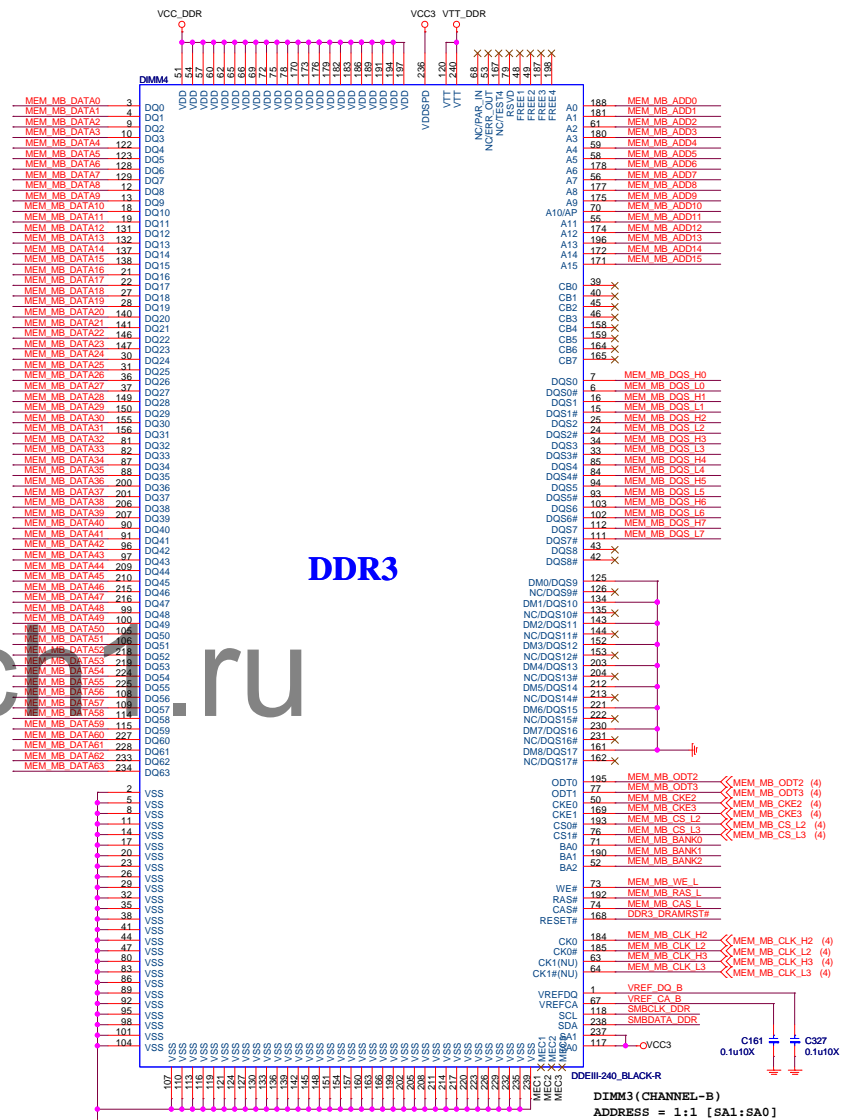
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Size	Document Description	Rev
Custom	DDR3 Channel-A DIMM1/2	1.0
Date: Monday, March 05, 2012	Sheet 7 of 41	

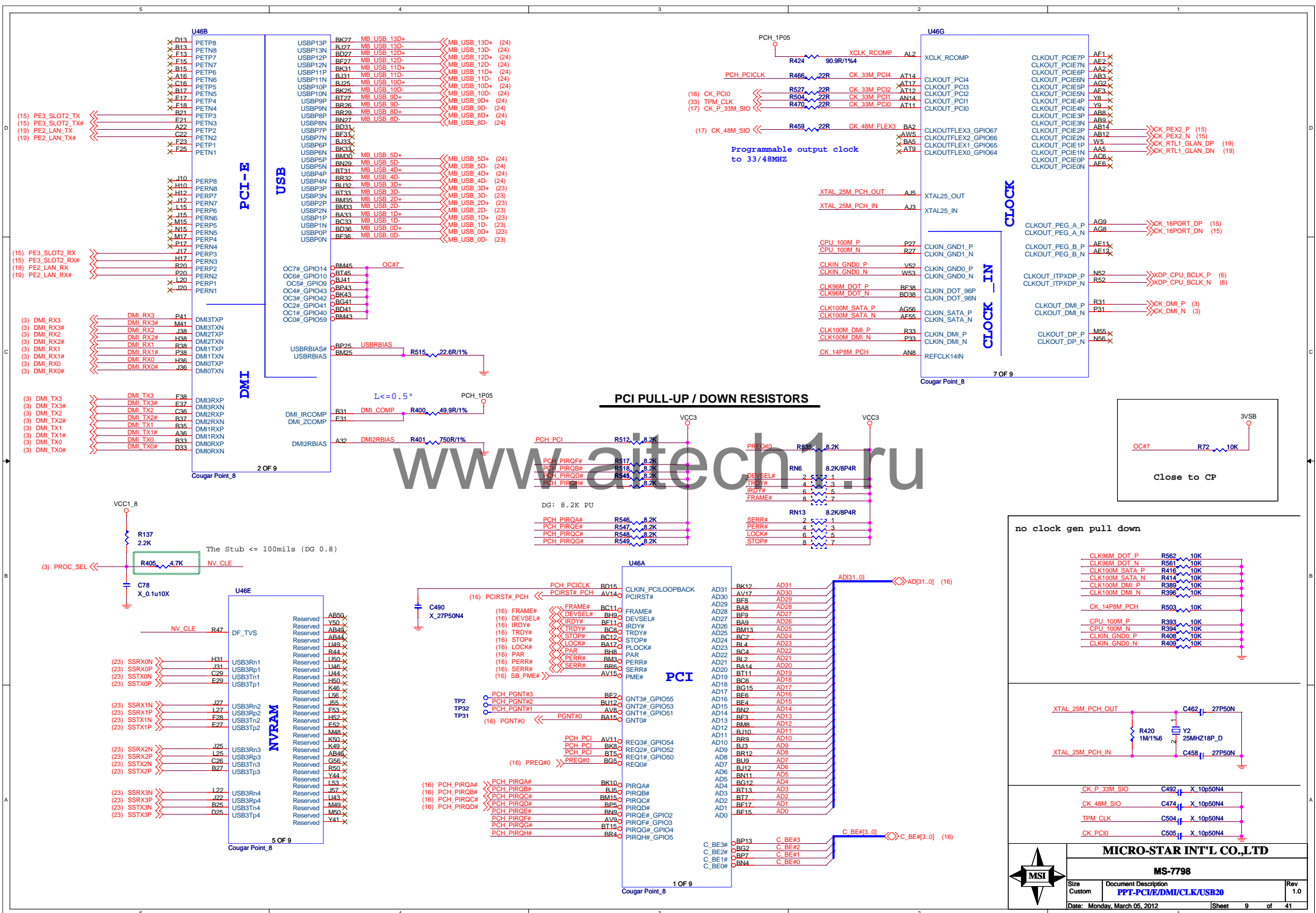
## DDRIII DIMM B0



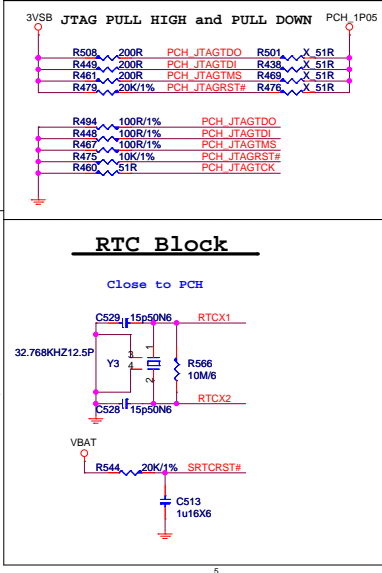
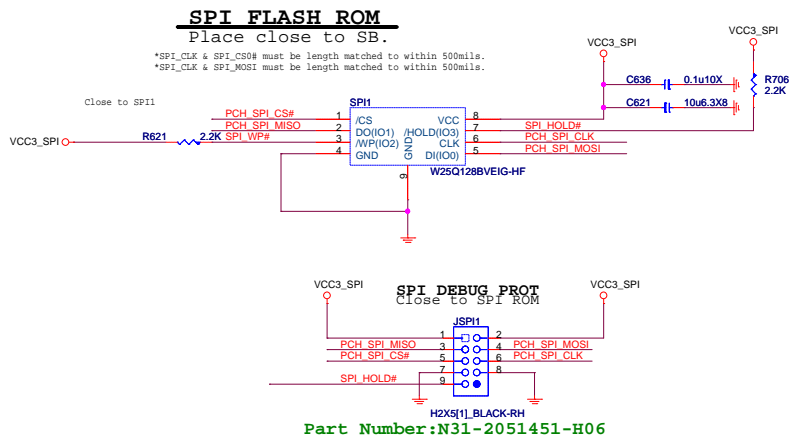
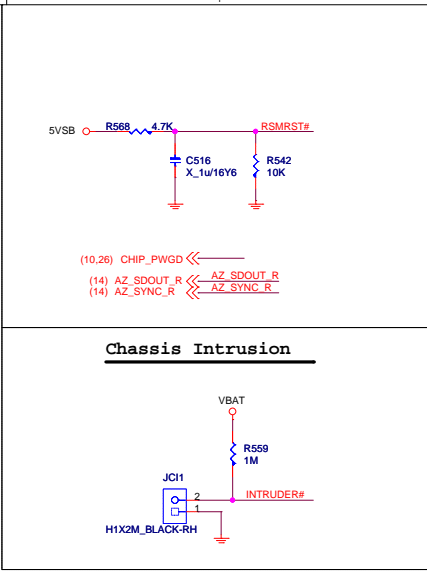
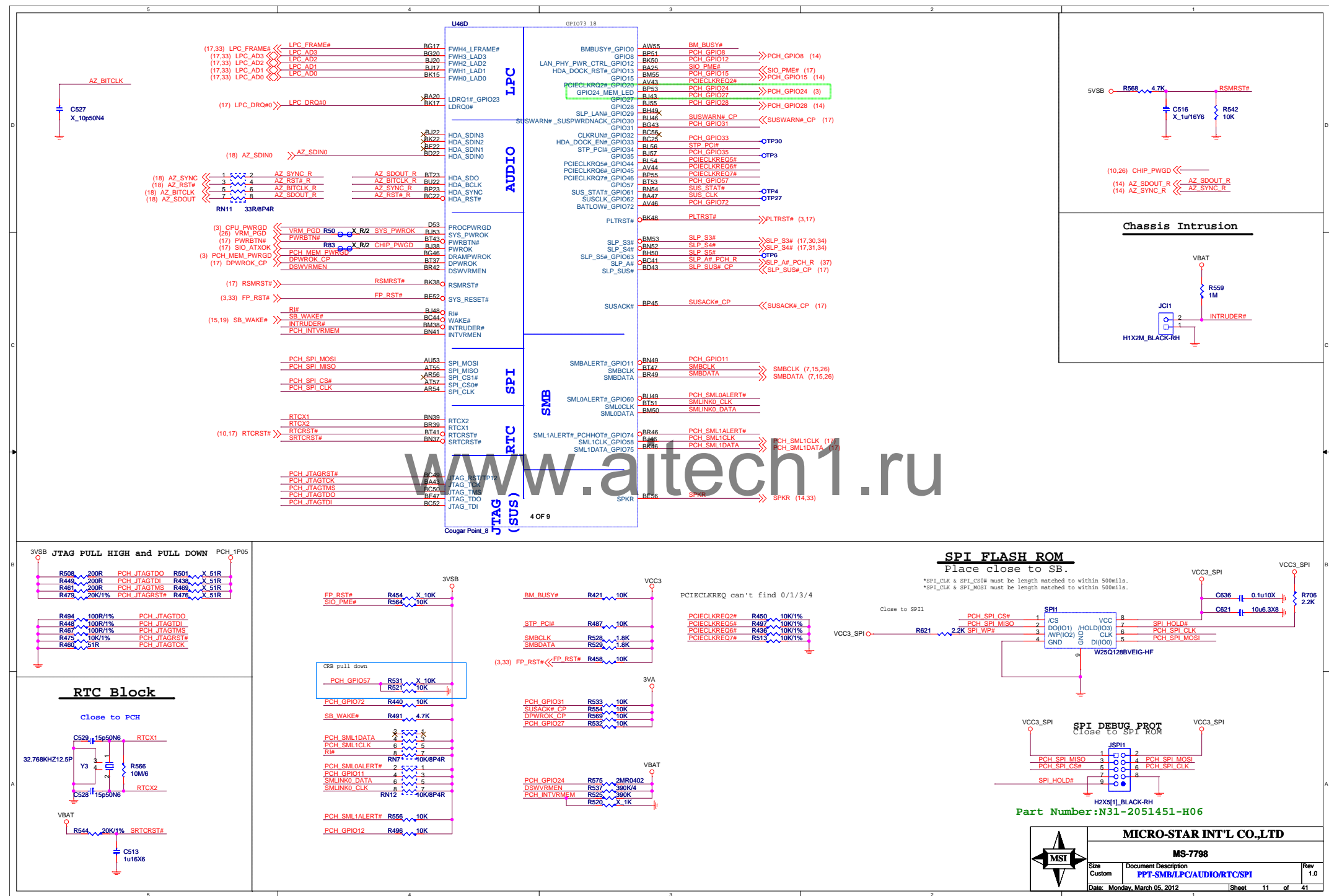
## DDRIII DIMM B1



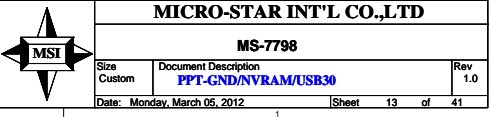












# PCH Straps

VCC3 — R457 X 10K — SPKR (11,33)

SPKR  
0 : Default Mode:  
1 : No Reboot Mode with TCO Disabled:

(10) INIT3\_3V# — R505 X 1K —  
Internal pull-up

Do not pull low.

3VSB — R565 X 1K — AZ\_SYNC\_R (11)

HDA\_SYNC  
OD PLL VR SUPPLY SEL  
0: 1.8V SUPPLY \*  
1: 1.5V SUPPLY

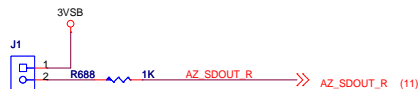
Internal weak pull down. Do not pull up.

3VSB — R506 X 1K — PCH\_GPIO15 (11)

Enable TLS:GPIO15  
Pull up with 1k Ohm to VccSus3.3.  
Default (Disable TLS):  
Leave NC. Internal pull down.

(11) PCH\_GPIO8 — R530 1K —

BTM  
Leave floating. Do not pull low.  
FCIM  
Pull low with 1k Ohm to ground.  
FCIM. Can be override by  
softstrap through ME.



Default  
Do not pull high.  
Disable ME in Manufacturing Mode  
Connect to VccSusHDA with 1k Ohm pull-up  
resistor through a jumper.

(11) PCH\_GPIO28 — R481 X 1K —

Internal weak pull up. Do not pull low.  
On die PLL voltage regulator

(10) PCH\_GPIO36 — PCH\_GPIO36 — R428 X 10K/1% — R430 X 10K/1% — VCC3

Since Pin has strap functionality that requires internal pull-down to be sampled at rising PWROK, following guidelines are required to be followed:

- When Used as SATA2GP/SATA3GP for Mechanical Presence detect - Use a weak external pull-up (150K-200K ohms) to Vcc3\_3 OR use 10K external pull-up that is enabled only after PLTRST# de-assertion.
- When Used as GP Input (Pin HW default) - Ensure GPI is not driven high during strap sampling window  
When Unused as GPIO or SATA[x]GP - Use 8.2K-10K pull-down to ground.

(10) PCH\_GPIO37 — PCH\_GPIO37 — R474 X 10K/1% — R468 X 10K/1% — VCC3

Since Pin has strap functionality that requires internal pull-down to be sampled at rising PWROK, following guidelines are required to be followed:

- When Used as SATA2GP/SATA3GP for Mechanical Presence detect - Use a weak external pull-up (150K-200K ohms) to Vcc3\_3 OR use 10K external pull-up that is enabled only after PLTRST# de-assertion.
- When Used as GP Input (Pin HW default) - Ensure GPI is not driven high during strap sampling window  
When Unused as GPIO or SATA[x]GP - Use 8.2K-10K pull-down to ground.



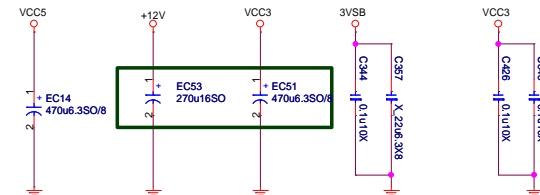
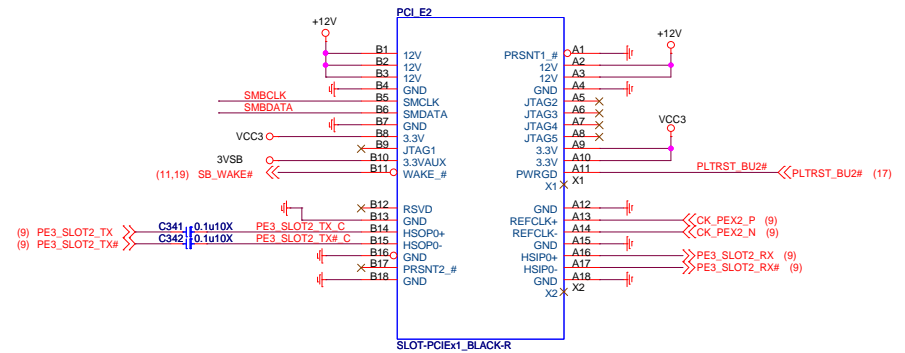
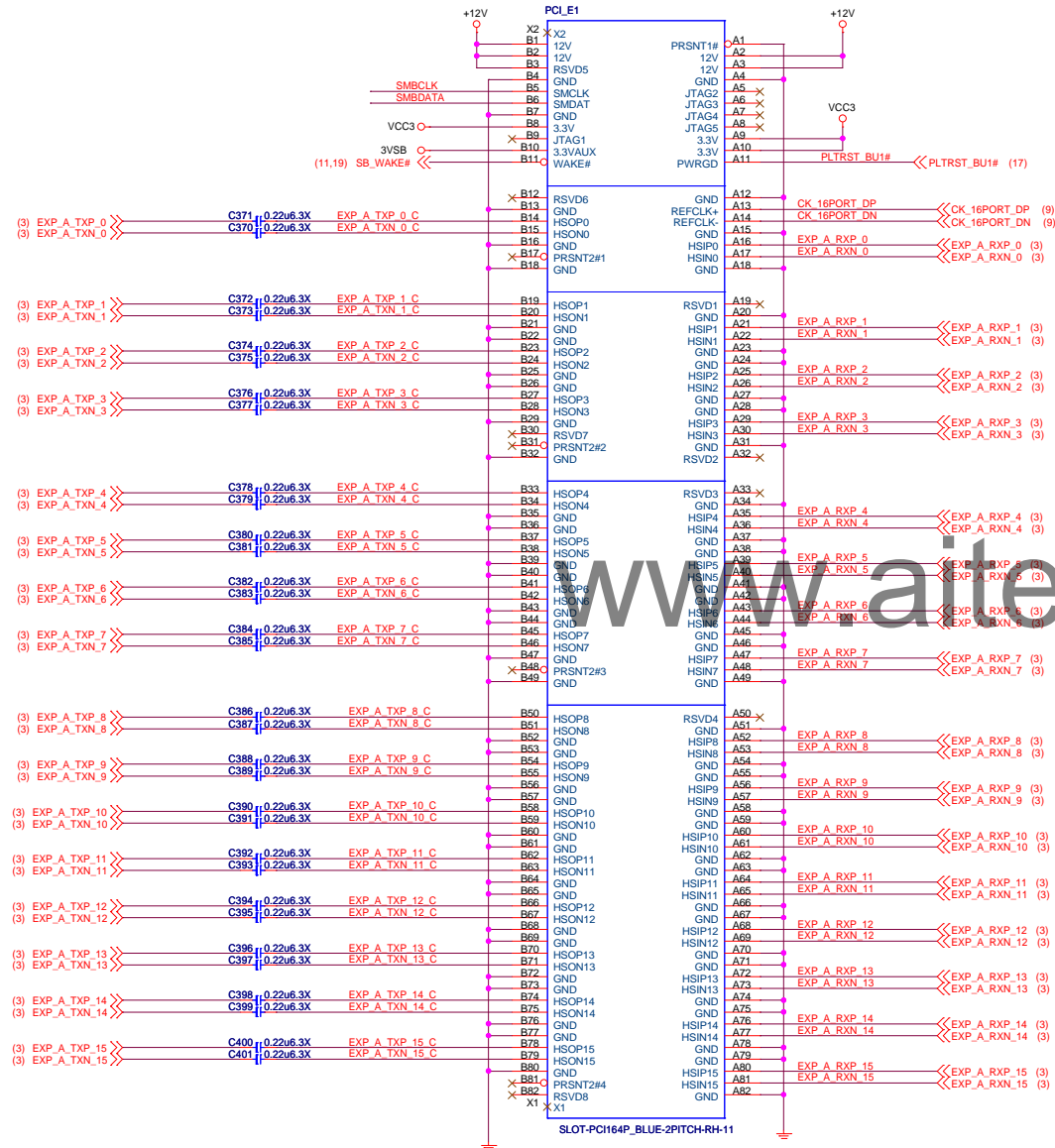
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MS-7798

Size	Document Description	Rev
Custom	PPT Strap	1.0
Date: Monday, March 05, 2012 Sheet 14 of 41		



(7,11,26) SMBCLK  
(7,11,26) SMBDATA



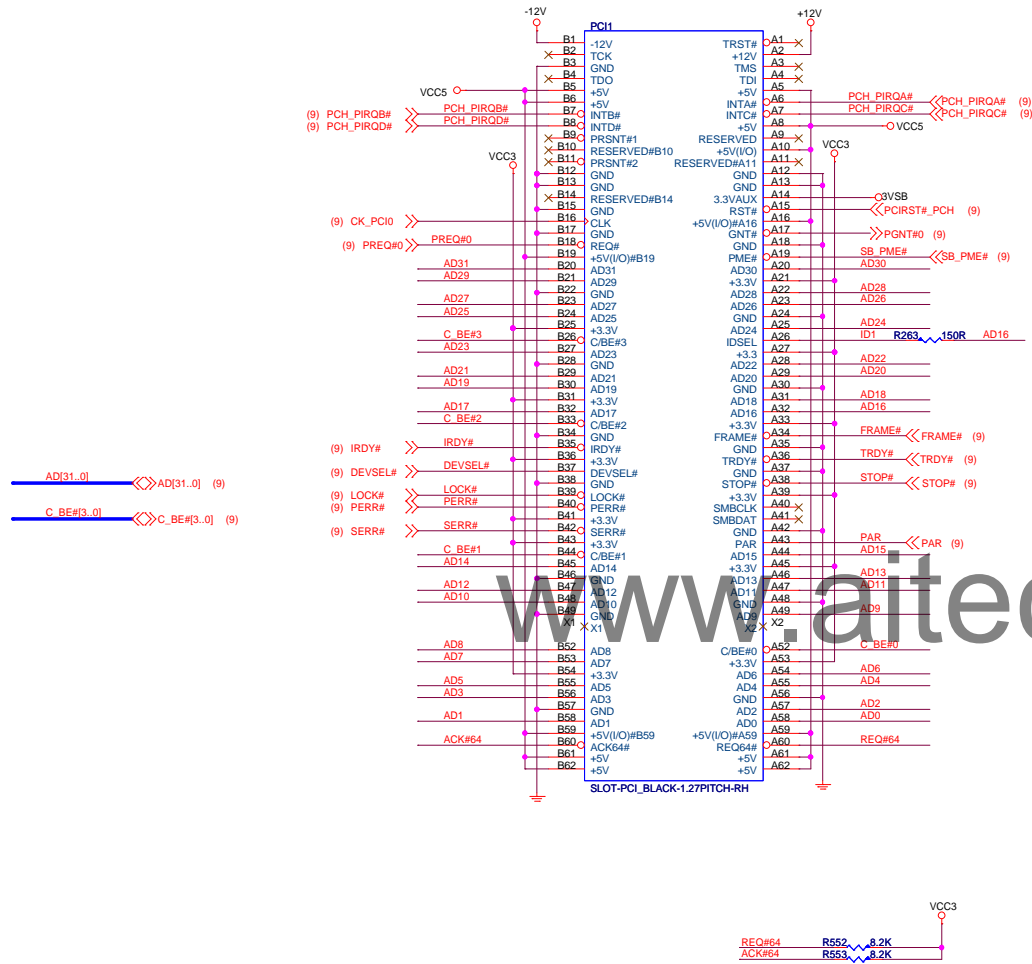
EL 470u16  
C94-4711611-R07



MICRO-STAR INT'L CO.,LTD

MS-7798

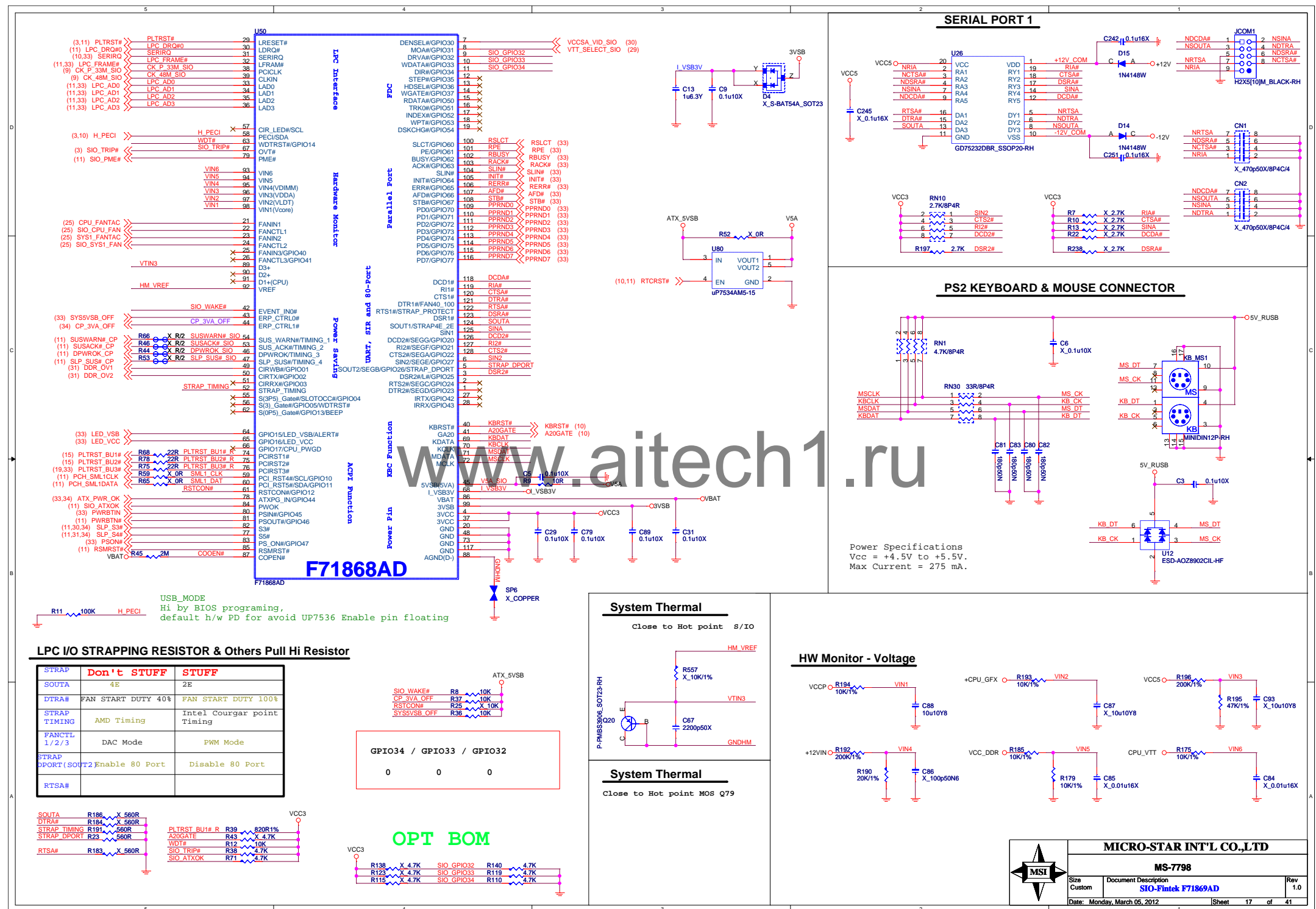
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Custom	PCIE1(X1) & PCIE2(X16) Slots	1.0
Date:	Monday, March 05, 2012	Sheet 15 of 41



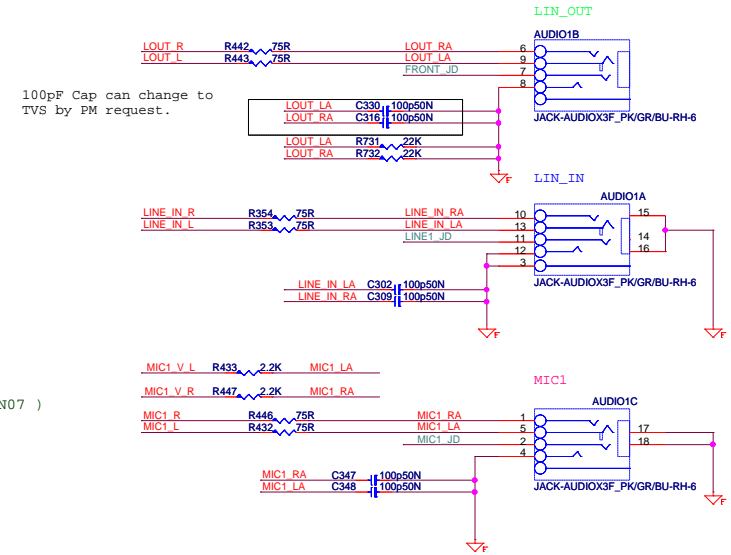
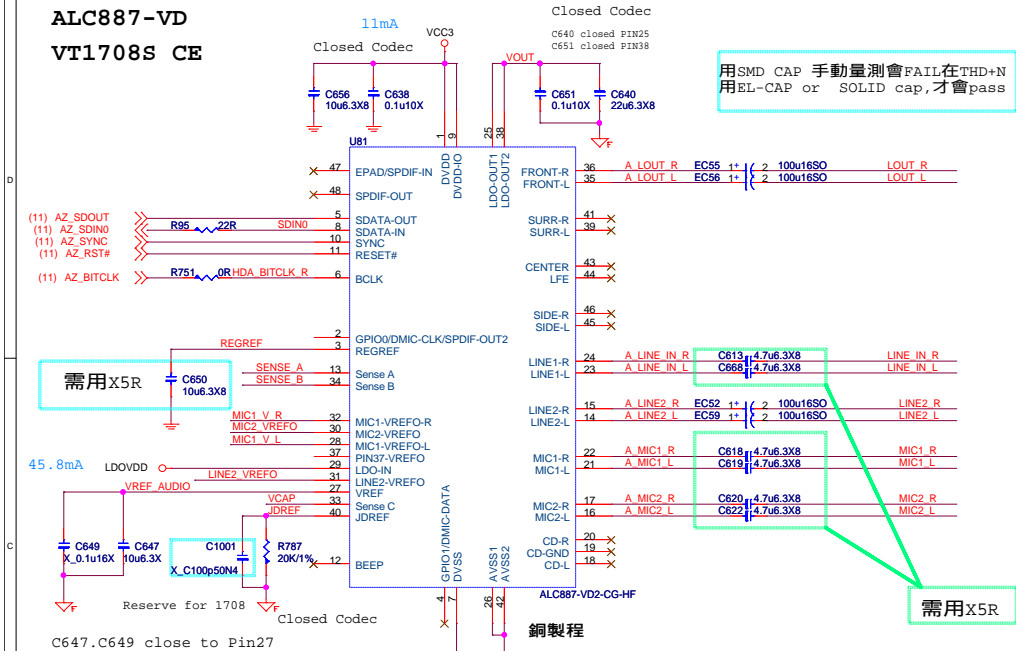
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MS-7798

Size	Document Description	Rev
Custom	PCI Slots	1.0
Date: Monday, March 05, 2012	Sheet 16 of 41	



ALC887-VD  
VT1708S CE

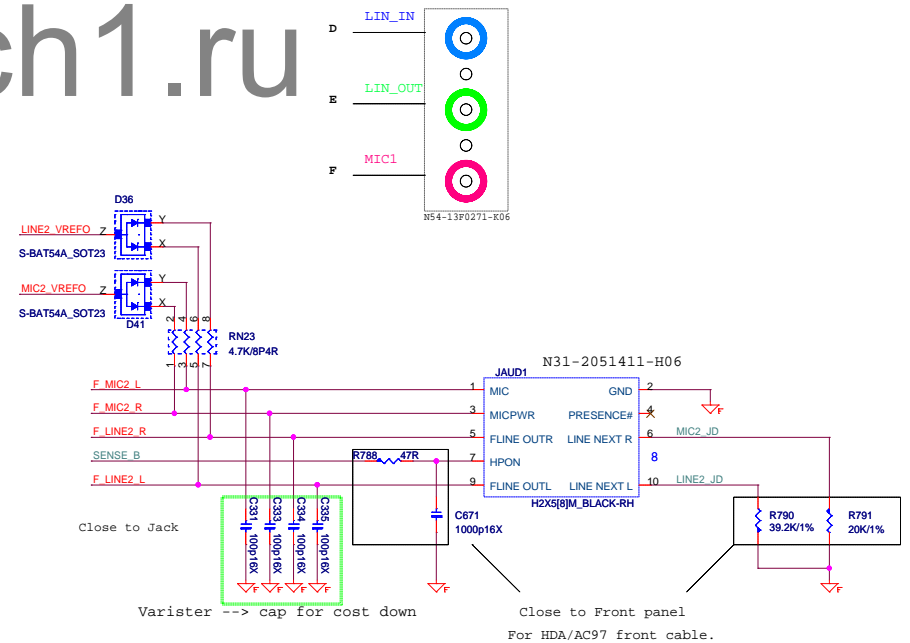
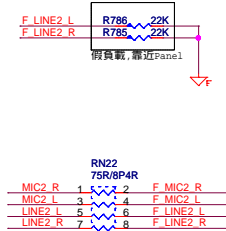
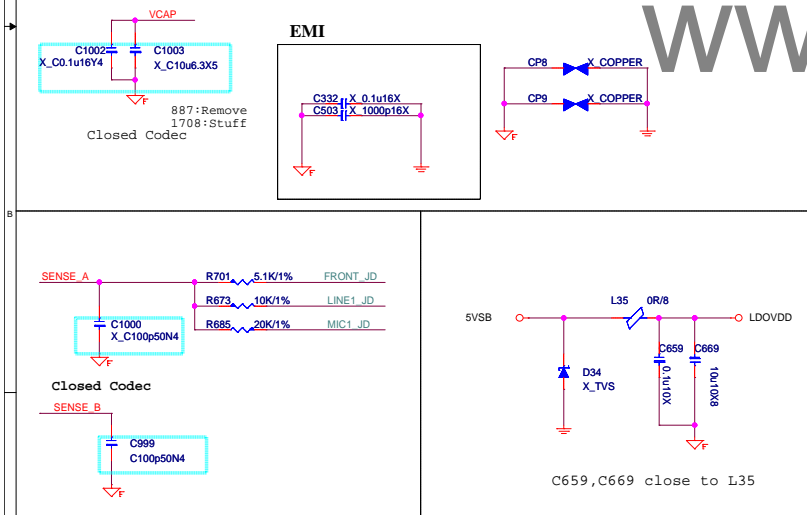


EL 100u ( C94-1012511-N07 )

需用X5R

4. 7k變為2.2k, 麥克風錄音效果變好

當串接電容有極性時，需上對地電阻



**MICRO-STAR INT'L CO.,LTD**

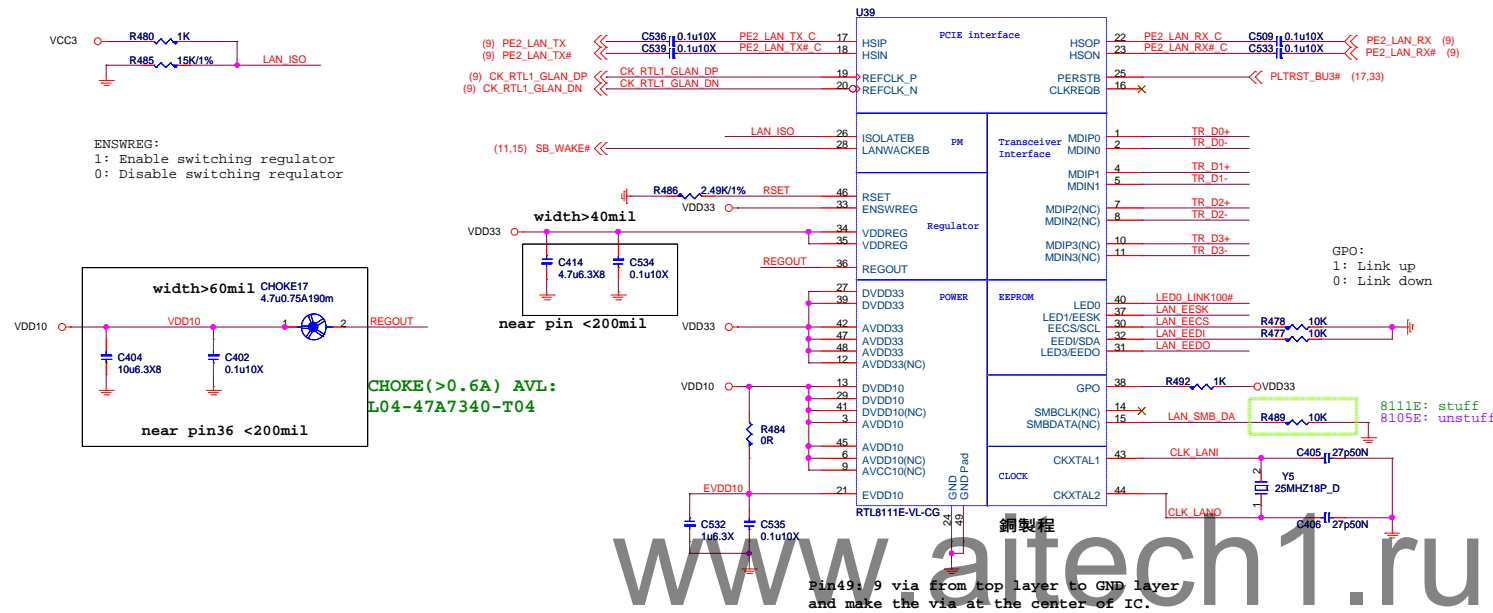
MS-7798

Size	Document Description
Custom	<b>Audio Codec ALC887/VT1708S</b>

Date: Monday, March 05, 2012 Sheet 18 of 41

# RTL8111E Giga LAN

# RTL8105E 10/100M LAN



## LAN Connector

Giga-Lan	10/100-Lan
<b>N58-22F0731</b>	<b>N58-22F0771</b>
Link Yellow	Link Yellow
Active Blinking	Active Blinking
1000 Orange	100 Green
100 Green	10 None
10 None	
19	19
20	20
21	21
22	22

## 8111E POWER Consumption

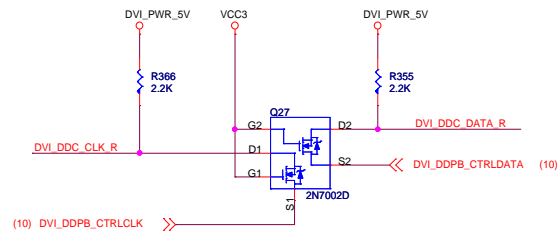
	3.3V	mW
10 M Idle/TxRx	12/66	40/218
100 M Idle/TxRx	31/44	102/145
Giga Idle/TxRx	135/163	452/538
ALDPS	4	13

## 8105E POWER Consumption

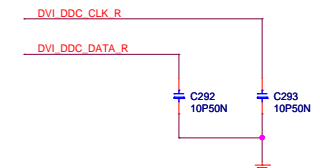
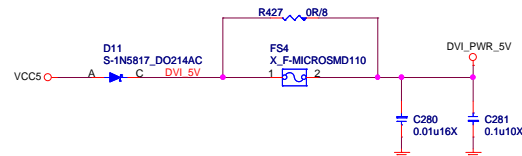
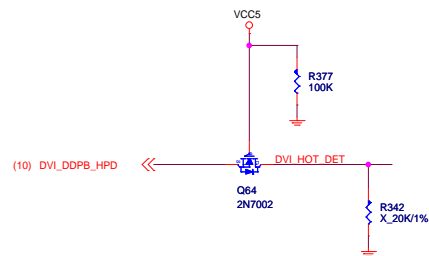
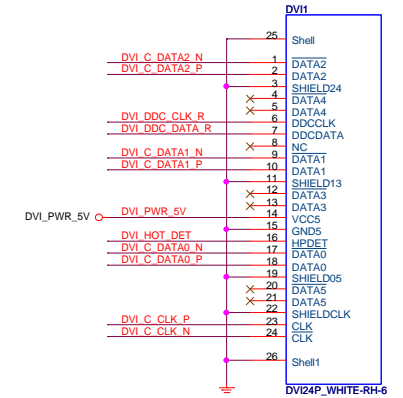
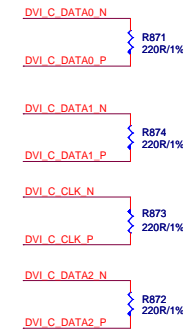
	3.3V	mW
10 M Idle/TxRx	14/75	46/248
100 M Idle/TxRx	43/66	142/218
S0 ALDPS	3.2	11

VGA: resolution of 2048x1536 pixels with 32-bit color at 75 Hz (4:3 QXGA)

(10) DVI\_DDPB\_CLK\_N C337 0.1u10X DVI\_C\_CLK\_N R498 680R DVI\_DATA\_CLK\_DN  
 (10) DVI\_DDPB\_CLK\_P C336 0.1u10X DVI\_C\_CLK\_P R524 680R DVI\_DATA\_CLK\_DP  
 (10) DVI\_DDPB\_TXN0 C362 0.1u10X DVI\_C\_DATA0\_N R493 680R DVI\_DATA0\_DN  
 (10) DVI\_DDPB\_TXN0 C361 0.1u10X DVI\_C\_DATA0\_P R507 680R DVI\_DATA0\_DP  
 (10) DVI\_DDPB\_TXP0 C338 0.1u10X DVI\_C\_DATA1\_N R519 680R DVI\_DATA1\_DN  
 (10) DVI\_DDPB\_TXN1 C339 0.1u10X DVI\_C\_DATA1\_P R523 680R DVI\_DATA1\_DP  
 (10) DVI\_DDPB\_TXP1 C364 0.1u10X DVI\_C\_DATA2\_N R526 680R DVI\_DATA2\_DN  
 (10) DVI\_DDPB\_TXN2 C363 0.1u10X DVI\_C\_DATA2\_P R514 680R DVI\_DATA2\_DP  
 (10) DVI\_DDPB\_TXP2



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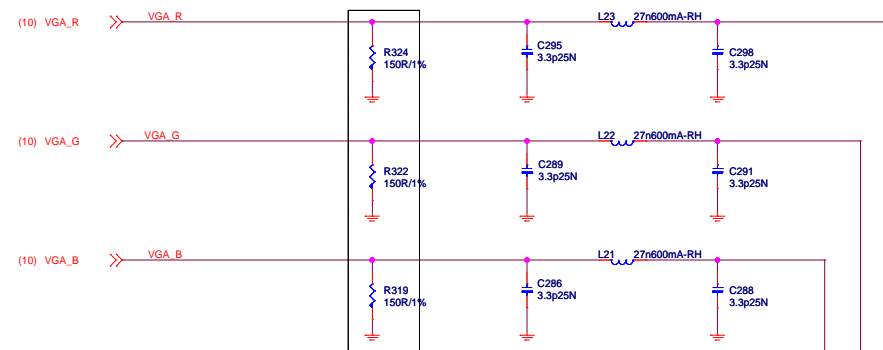
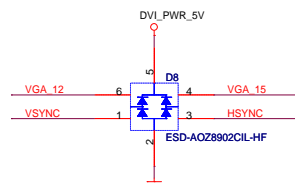
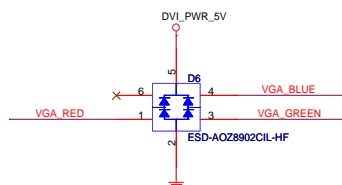
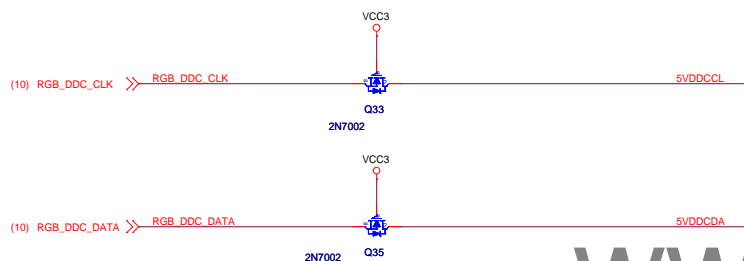
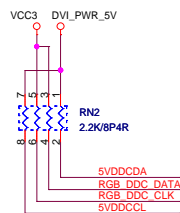
Size	Document Description	Rev
Custom	DVI Connector	1.0
Date: Monday, March 05, 2012	Sheet 20 of 41	



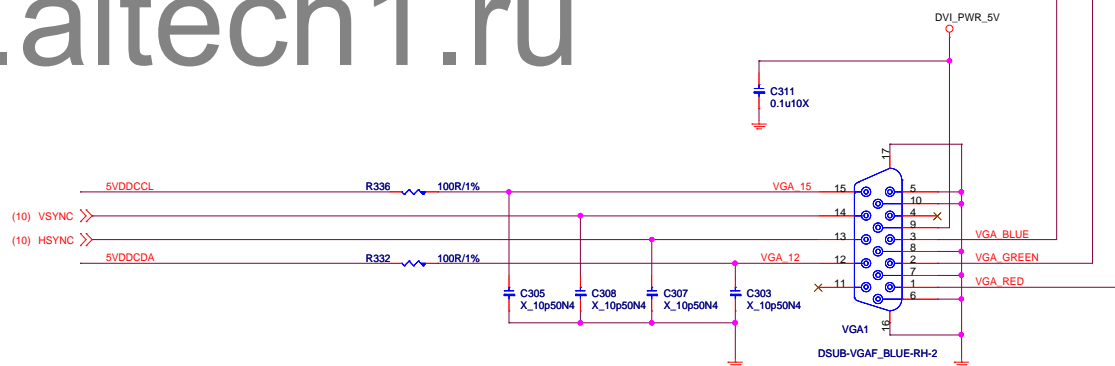
# D-Sub

## Level shift

VGA: resolution of 2048x1536 pixels with 32-bit color at 75 Hz (4:3 QXGA)

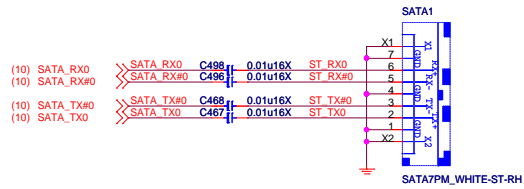


PLACE CLOSE TO VGA CONNECTOR,  
WITHIN 750 MIL OF PIN

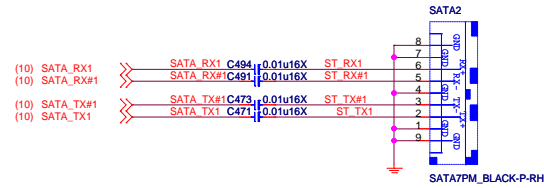


MSI			
MICRO-STAR INT'L CO.,LTD			
MS-7798			
Size	Document Description	Rev	
Custom	VGA Connector	1.0	
Date: Monday, March 05, 2012		Sheet	21 of 41

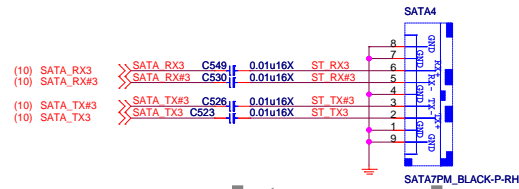
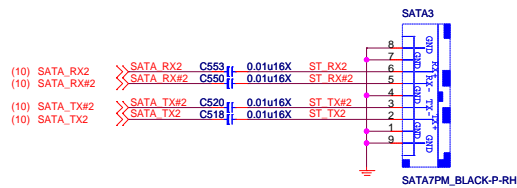
**SATA1**  
3.0  
white



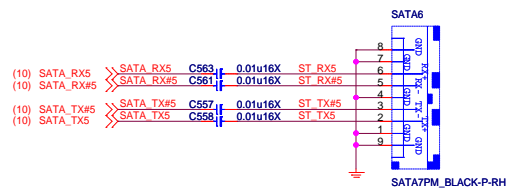
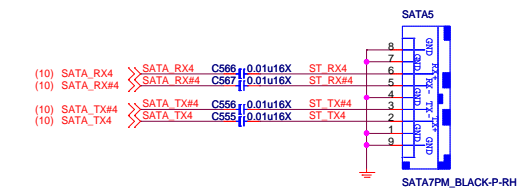
**SATA2**



**SATA3-4**

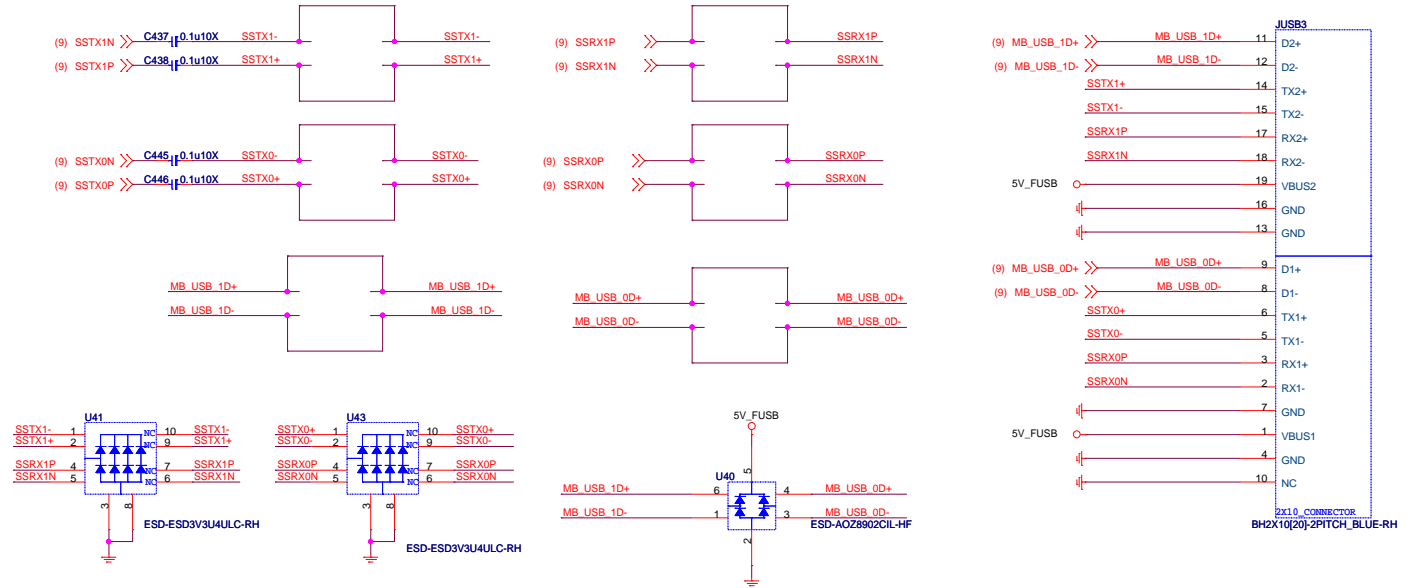


**SATA5-6**

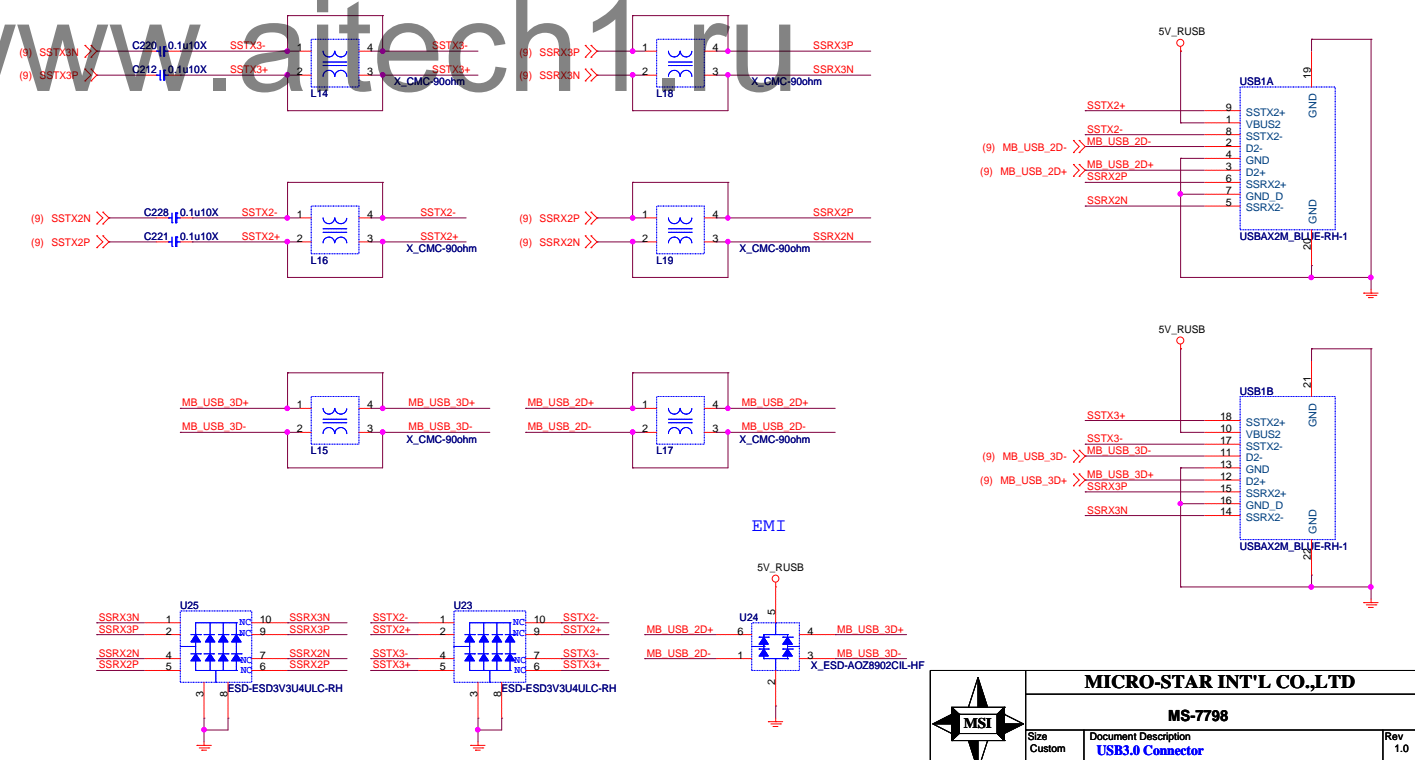


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# FRONT USB30 PORT 0,1



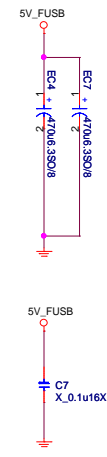
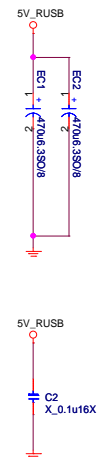
# REAR USB30 PORT 2,3



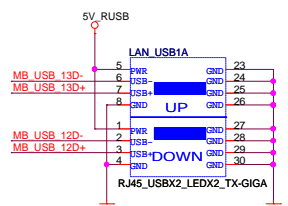
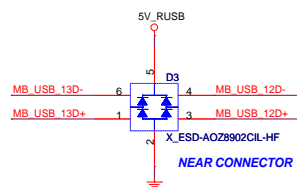
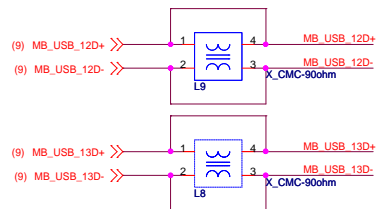
Near Rear ==>



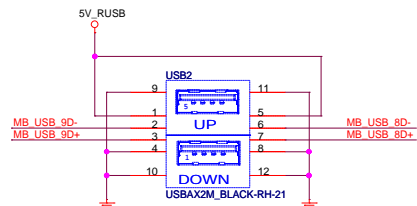
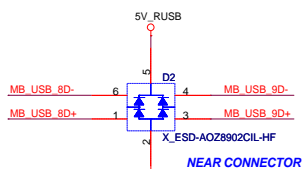
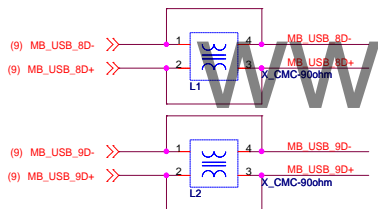
Near Front ==>



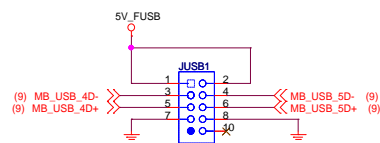
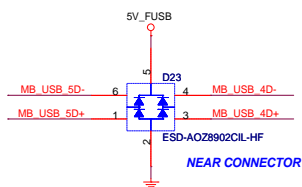
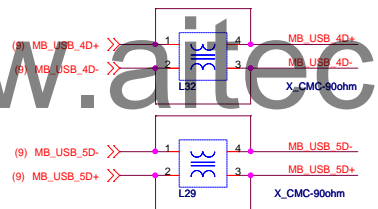
#### REAR USB PORT 8,9 (With PS2)



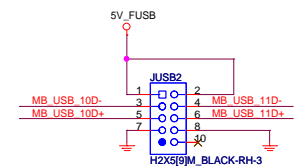
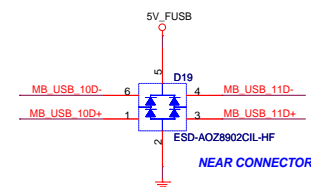
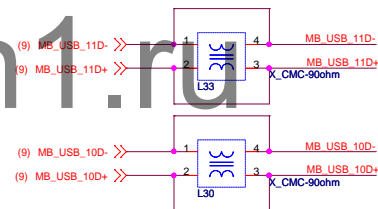
#### REAR USB PORT 8,9 (With PS2)



#### FRONT USB PORT 0,1



#### FRONT USB PORT 8,9



#### FRONT USB PORT 10,11

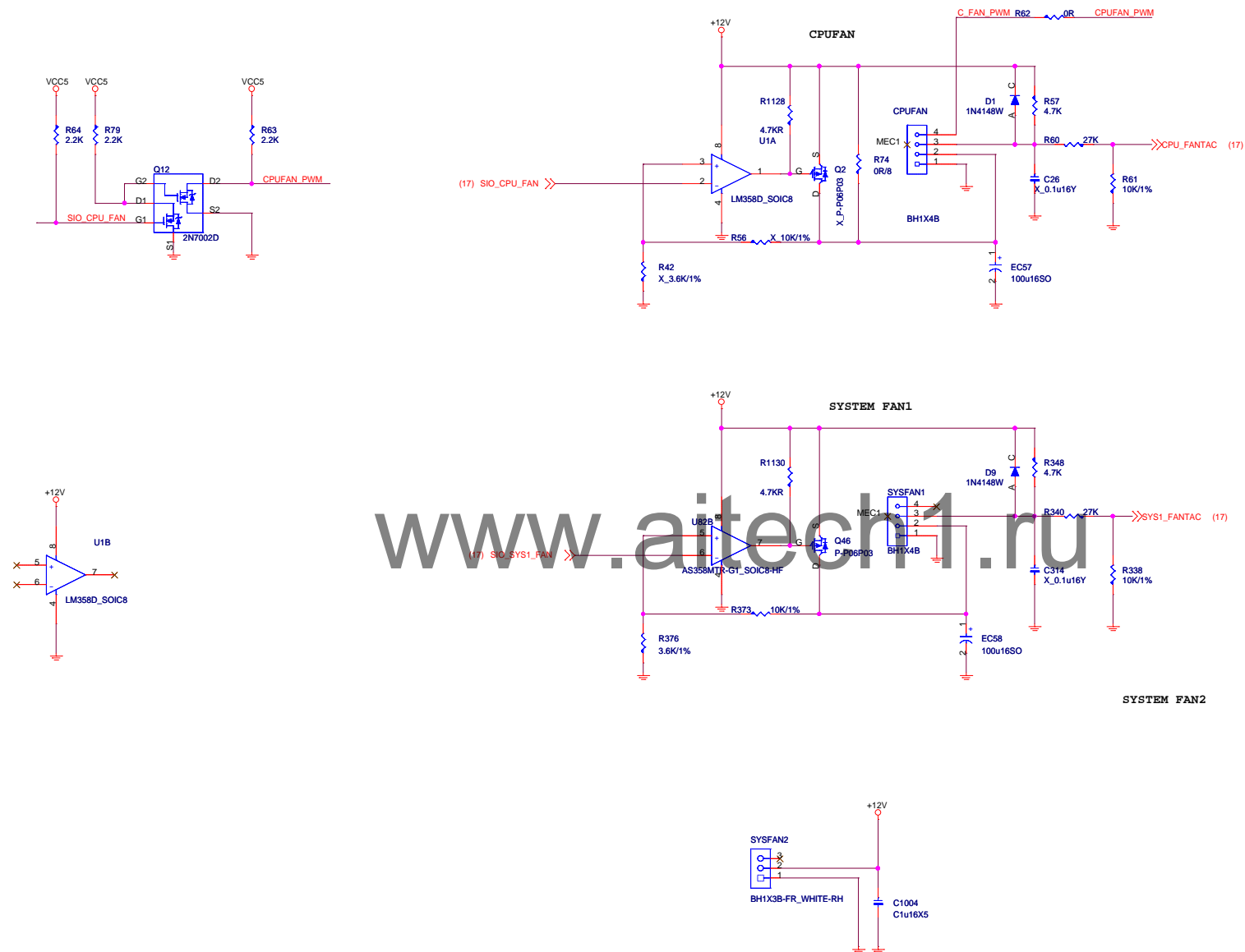


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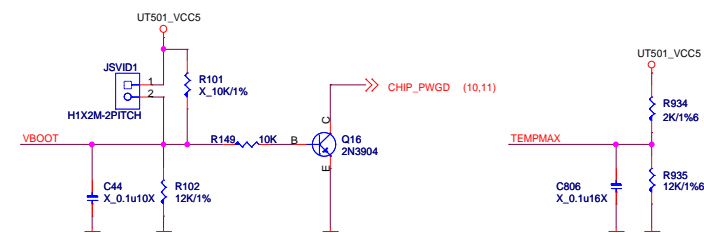
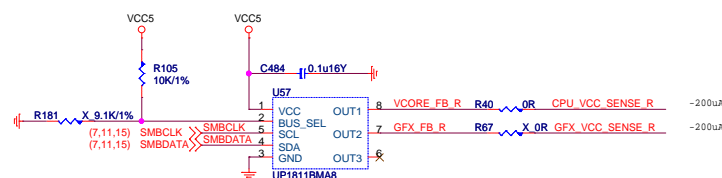
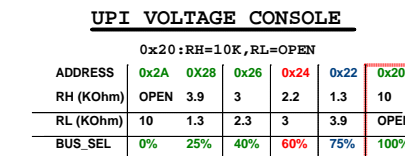
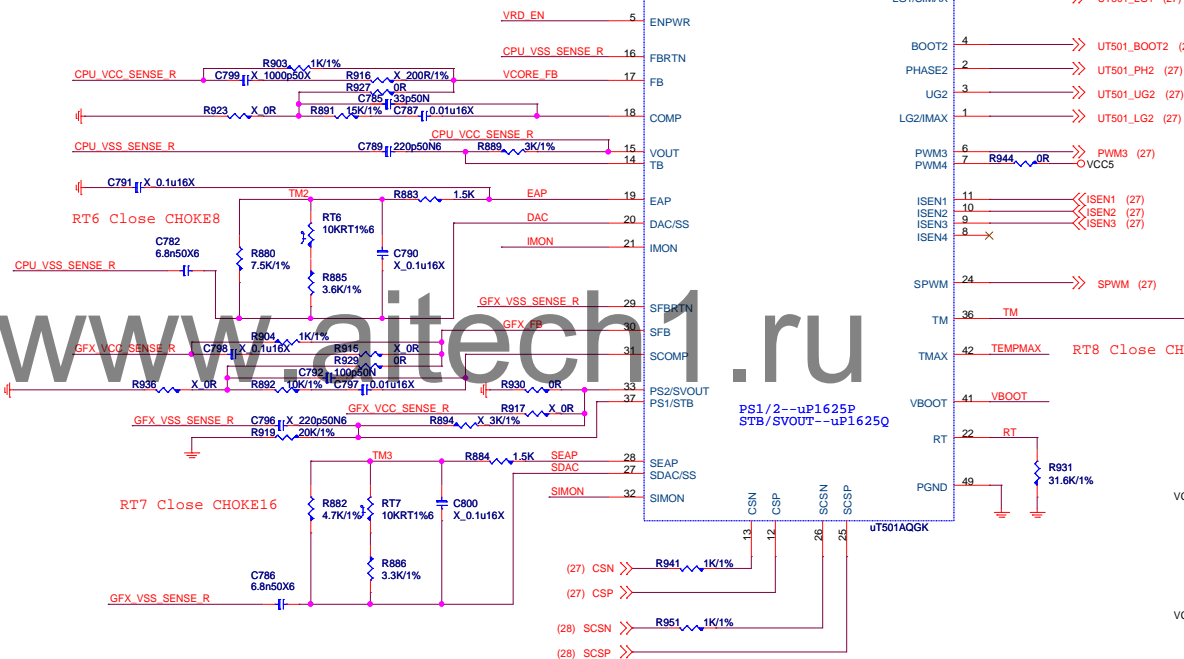
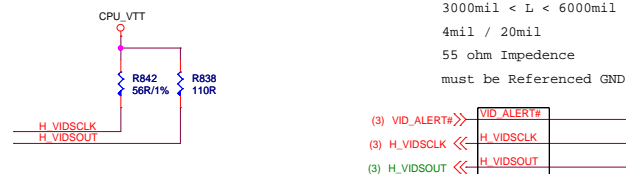
Size	Document Description	Rev
Custom	Rear I/O & USB2.0 Connector	1.0
Date: Monday, March 05, 2012	Sheet 24 of 41	

# FAN-COUNTROL CIRCUIT



## UT501 colay 1625q+

GFX      OCP: 52A



MS-7798

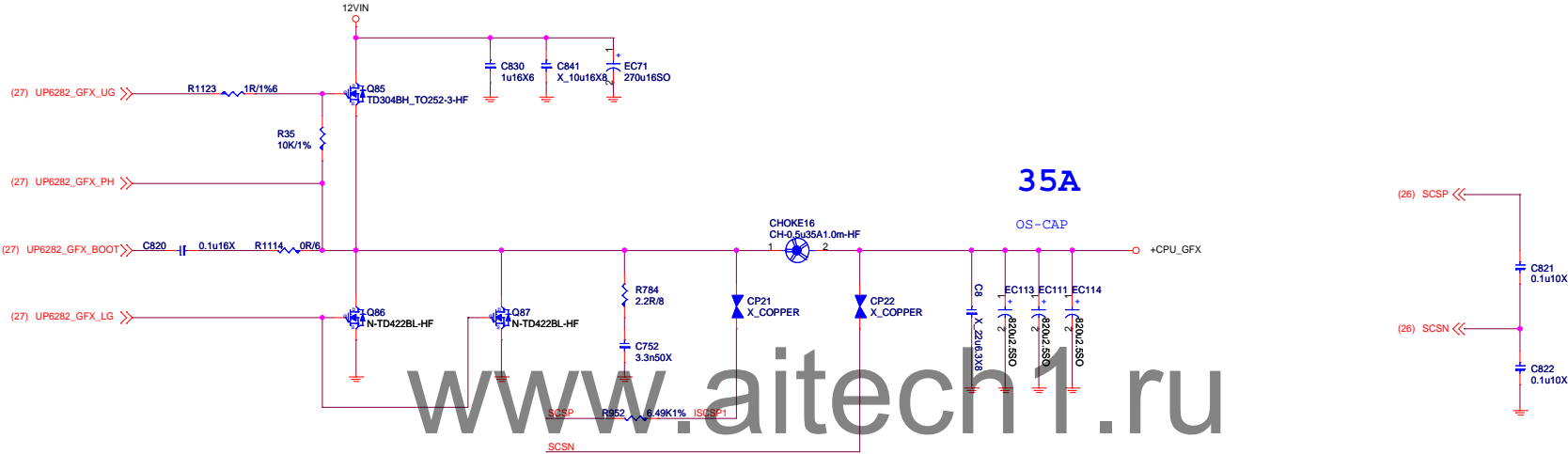
Size Custom	Document Description <b>VRD12 - PWM-UT501</b>	Rev 1.0
Date: Monday, March 05, 2012		Sheet 26 of 41





CPU\_GFX:0.25-1.52

35A FOR CPU

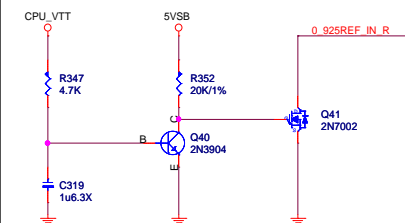


Date: Monday, March 05, 2012 Sheet 29 of 41

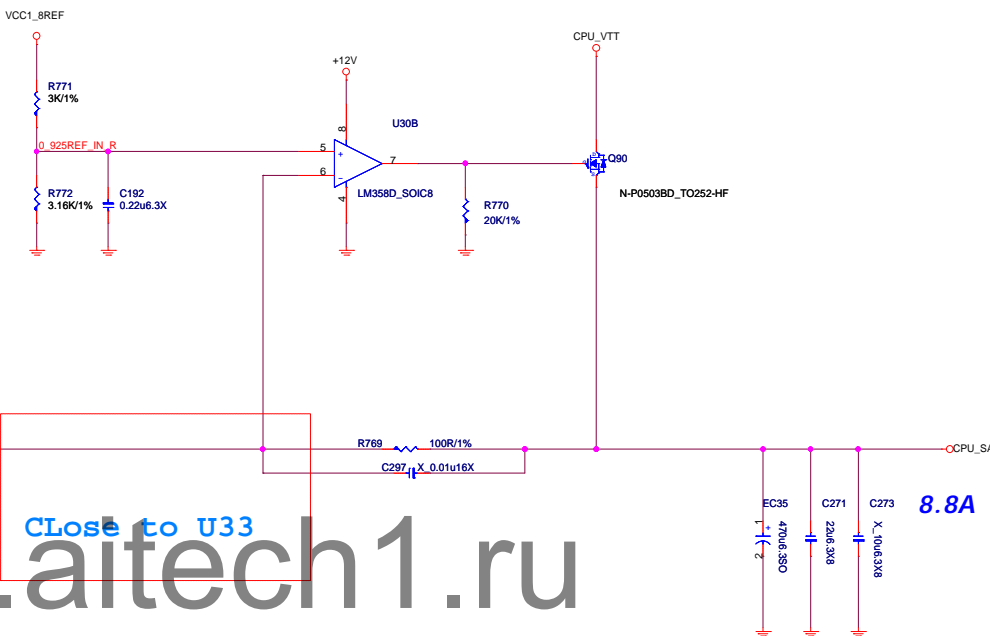
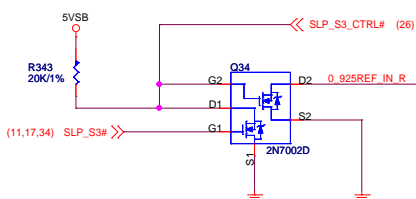
CPU\_SA:0.925/0.85

SA Core =8.8A

Waitting CPU\_VTT Ready



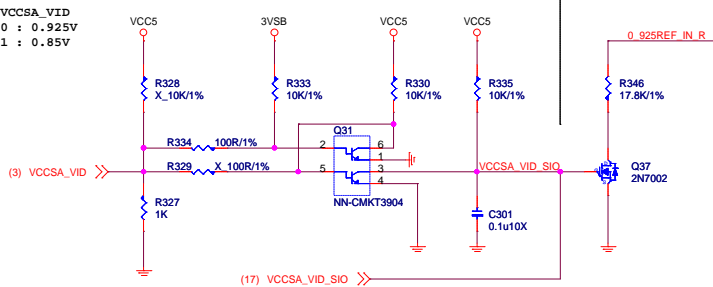
CRB



VCCSA_VID	
Low	0.925V
High	0.85V

VCCSA_VID_SIO Table	
Low	0.925V
High	0.85V

VCCSA_VID_SIO Table	
Ivy Bridge	0.85V
Sandy Bridge	0.925V

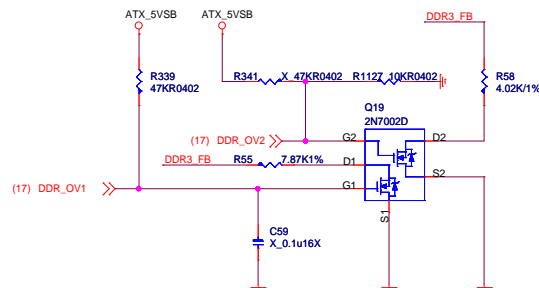


# DDR Power:1.5V

DDR3\_1.5V FOR CPU+FOR 4DIMM+FOR DDR\_VTT=15.7A

Iripple=8A  
4.7\*2\*1=9.4A>8A

## DDR OV

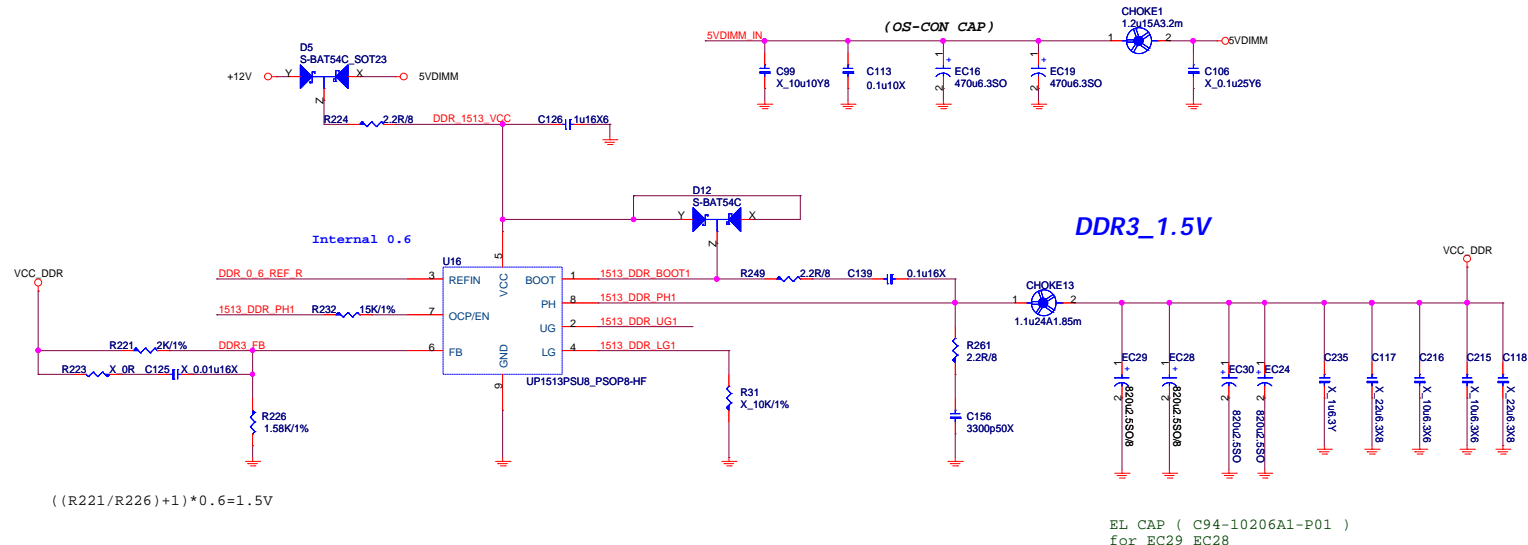


\*Default 1.5V

DDR_OV	1.35V	1.5V	1.65V	1.8V
DDR_OV1	Low	High	Low	High
DDR_OV2	Low	Low	High	High

DDR\_OV1 = GPIO01(S/IO)

DDR\_OV2 = GPIO02(S/IO)

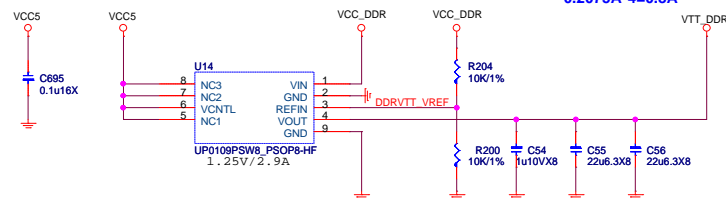


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## DDR VTT Power

To CPU Copper trace width > 250mils , Fill island behind DIMM > 400mils .

0.2075A\*4=0.8A



P.S. Only for meet Intel power down sequence.



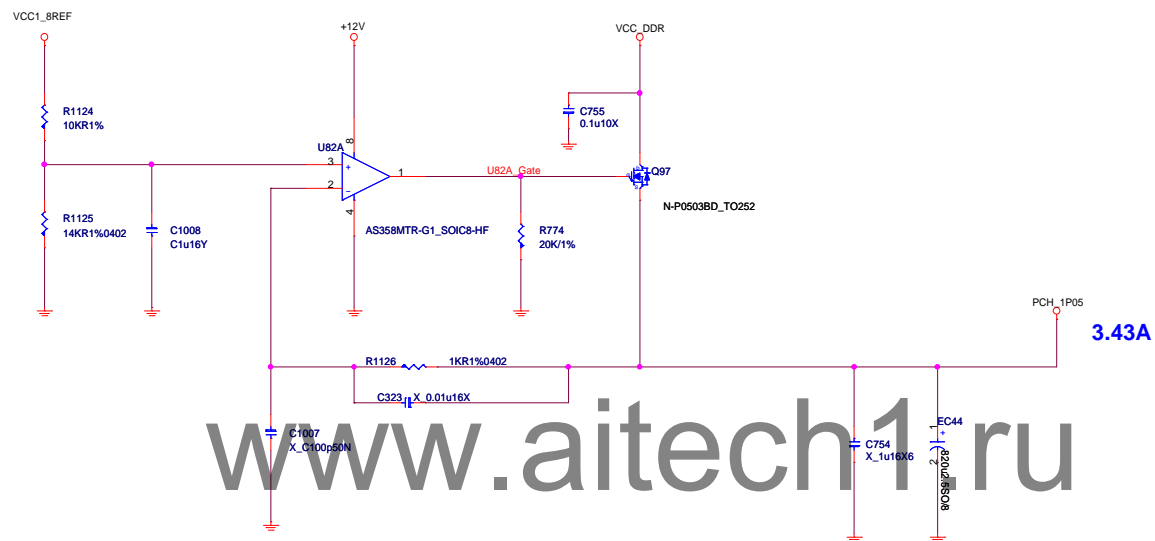
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MS-7798

Size Custom	Document Description	Rev 1.0
DDR Power -UP1513 1-Phase MOS		
Date: Monday, March 05, 2012	Sheet 31 of 41	

PCH Power:1.05V

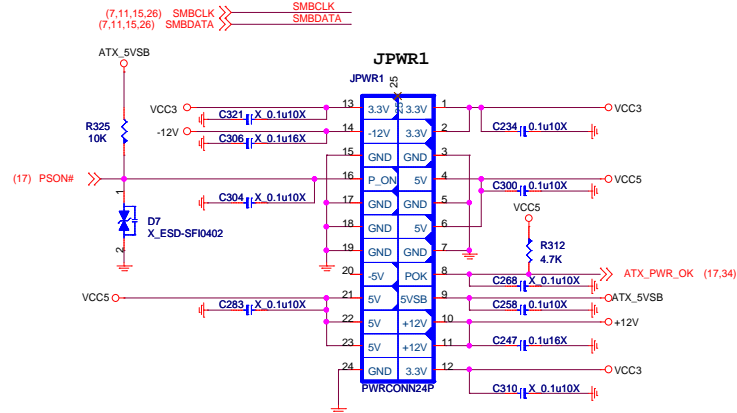
PCH Core =3.43A



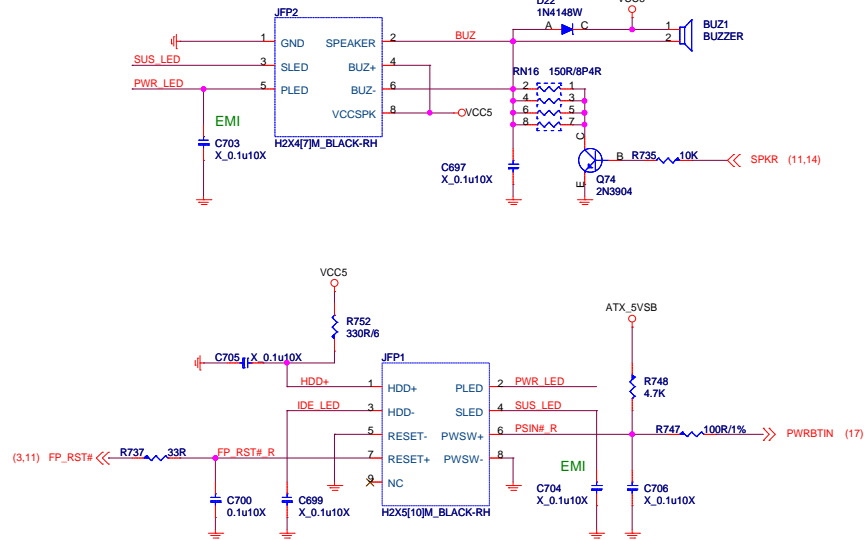
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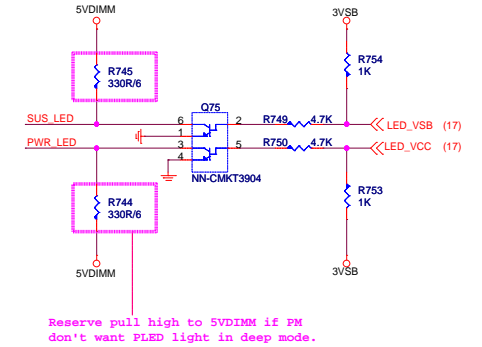
## ATX POWER CONNECTOR



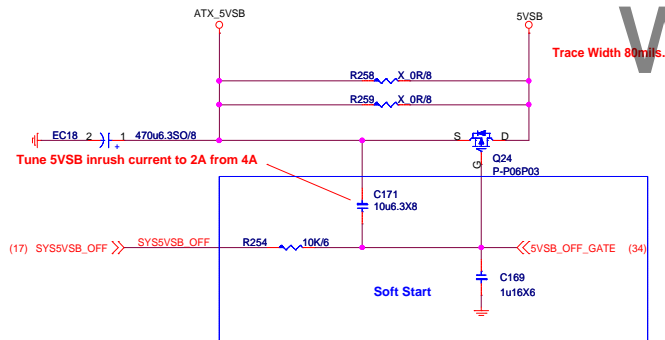
## FRONT PANNEL



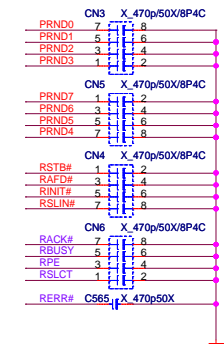
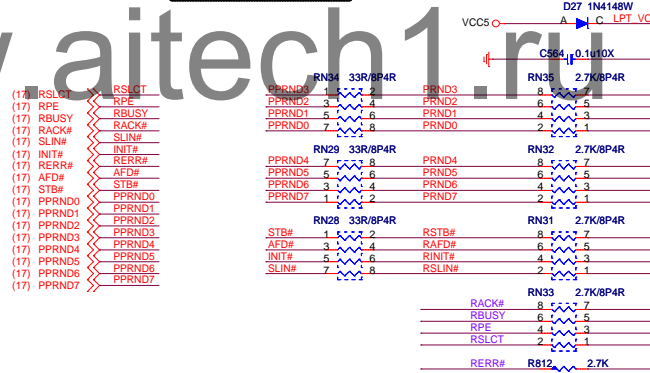
## LED ( for Fintek 71869)



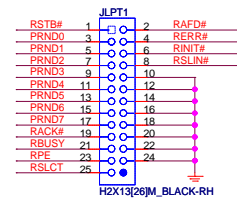
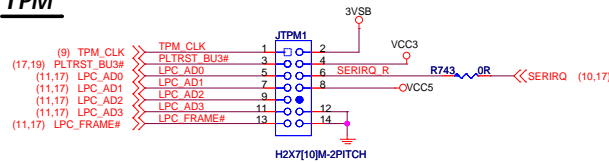
## 5VSB Power Switch



## PARALLAL PORT

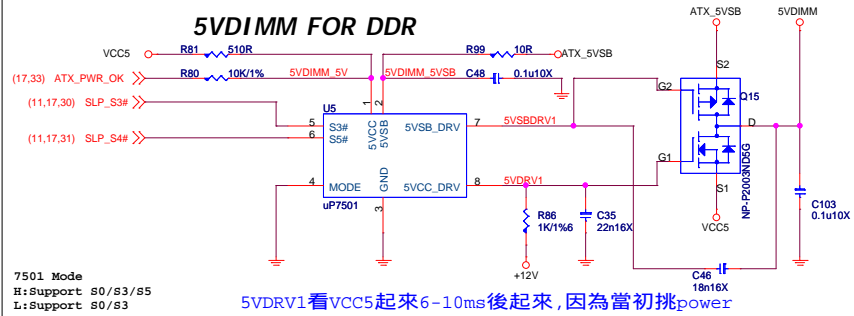


## TPM

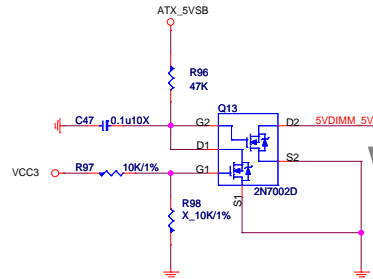
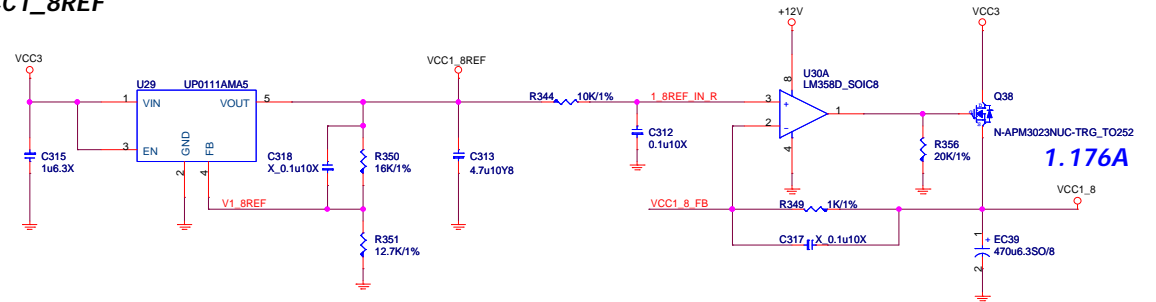


N31-2131151-H06 : 2.0mm  
N31-2131131-H06 : 2.54mm

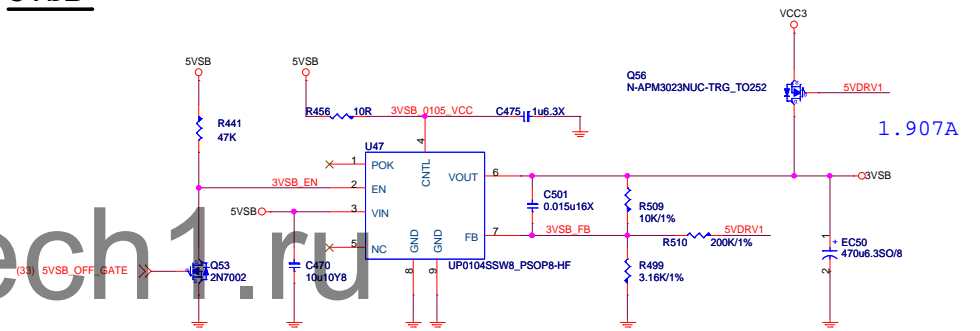
## 5VDIMM FOR DDR



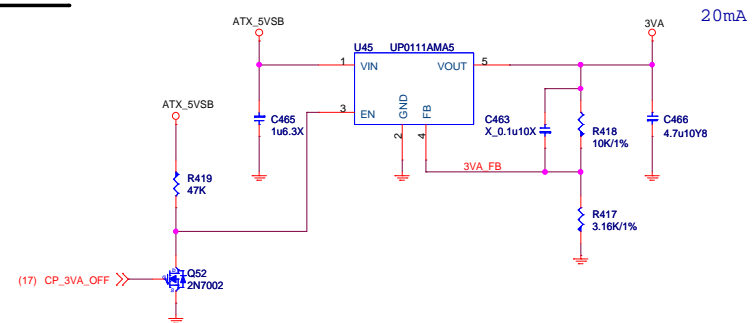
## VCC1\_8REF



## 3VSB



## 3VA



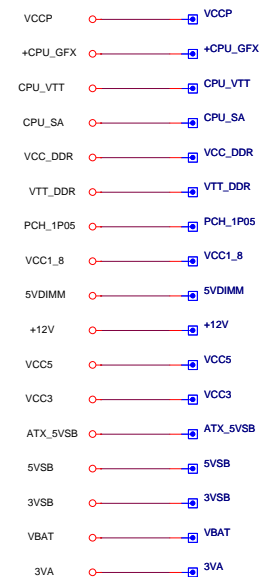
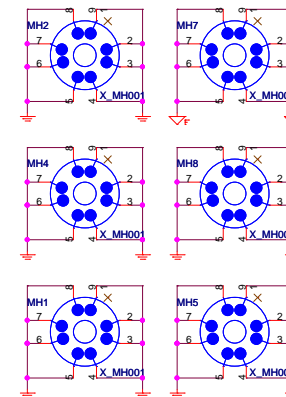
MICRO-STAR INT'L CO.,LTD

MS-7798

Size	Document Description	Rev
Custom	ACPI controller UPI	1.0
Date: Monday, March 05, 2012	Sheet 34 of 41	

OPT	Configure	BOM	Function

**Voltage test point**



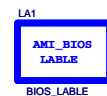
[www.aitech1.ru](http://www.aitech1.ru)



HDMI Label Part Number



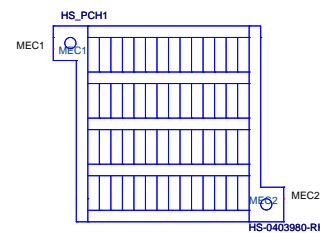
HDMI Virtual Part Number



BIOS\_LABEL



CPU\_H1



7798\_0A

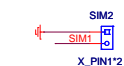
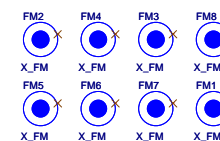
PK0-077980A-G37. 精成. 23. 寶安恩斯邁爾(MSIS)

PK0-077980A-G37, 精成, 2, 寶安恩斯邁廠(MSIS)

PK0-077980A-E48. 88華. 23. 寶安恩斯邁廟(MSIS)

PK0-077980A-E48, 雙華, 2, 寶安恩斯邁廠(MSIS)

## Simulation



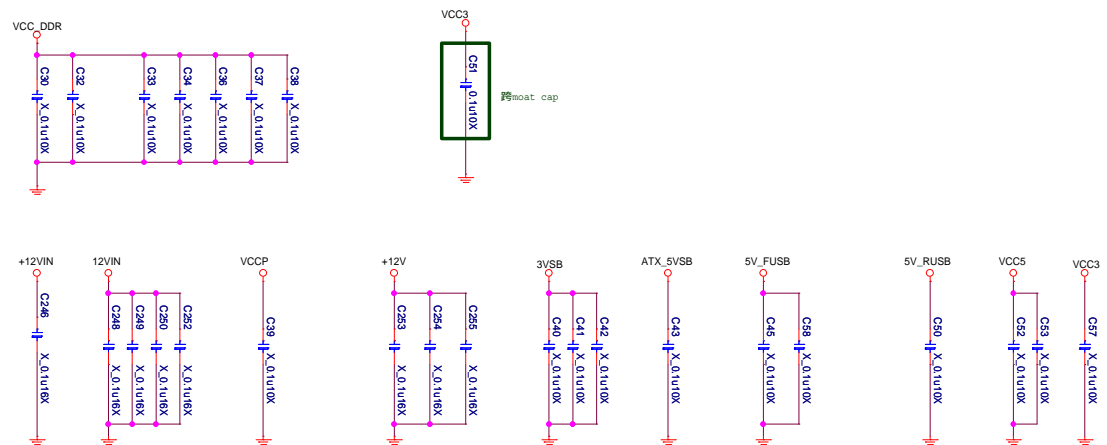
MS-7798

	Size Custom

Document Description	
<b>Manual Parts</b>	

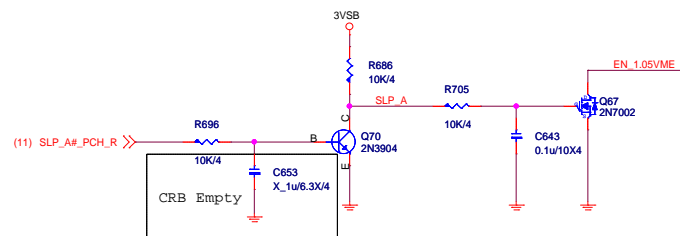
Date: Monday, March 05, 2012

Sheet 35 of 41



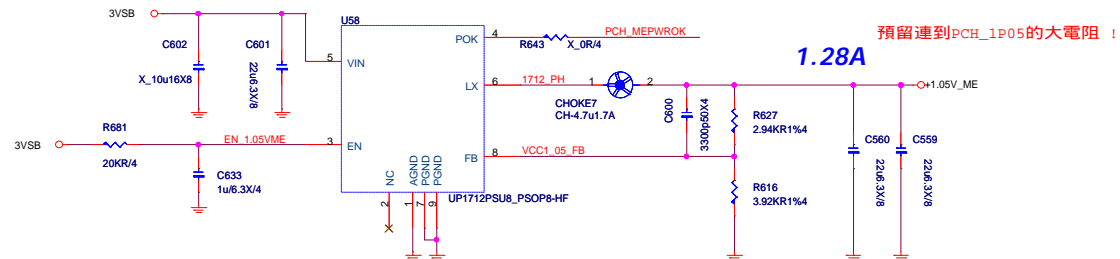
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## SLP\_A

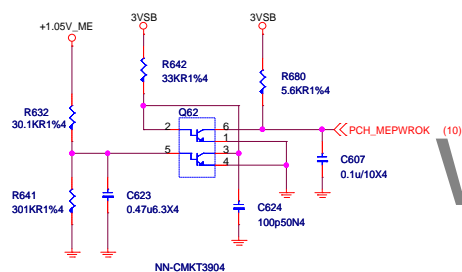


## ME Power Control

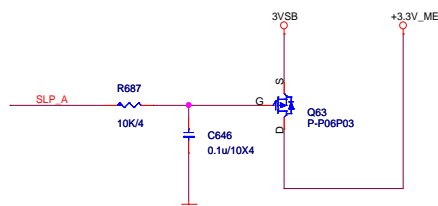
## +1.05V\_ME(VCCIO\_ME)



## PCH\_MEPWROK



## +3.3V\_ME



For INTEL ME BUG

